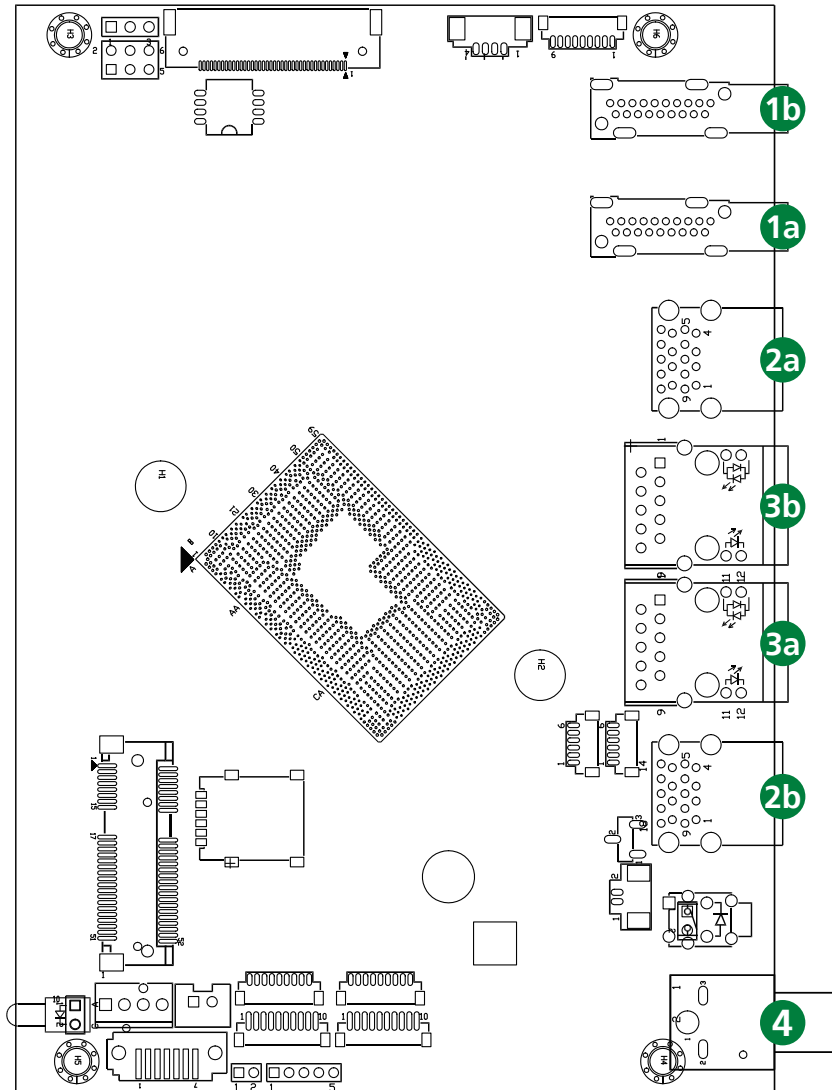


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1. HDMI1(1a), HDMI2(1b): HDMI Connector

Pin	Definition	Pin	Definition
1	HDMI1_TX2P	2	GND
3	HDMI1_TX2N	4	HDMI1_TX1P
5	GND	6	HDMI1_TX1N
7	HDMI1_TX0P	8	GND
9	HDMI1_TX0N	10	HDMI1_CLK_P
11	GND	12	HDMI1_CLK_N
13	NC	14	NC
15	HDMI1_SCL	16	HDMI1_SDA
17	GND	18	HDMI1_P5V
19	HDMI1_HPD	MH1	CGND
MH2	CGND	MH3	CGND
MH4	CGND		

2. USB3_12P(2a), USB3_34P(2b): USB 3.2 Connector

Pin	Definition	Pin	Definition
1	+5V	2	USB2_3N
3	USB2_3P	4	GND
5	USB3_RX3N	6	USB3_RX3P
7	GND	8	USB3_TX3N
9	USB3_TX3P	10	+5V
11	USB2_4N	12	USB2_4P
13	GND	14	USB3_RX4N
15	USB3_RX4P	16	GND
17	USB3_TX4N	18	USB3_TX4P
MH1	CGND	MH2	CGND
MH3	CGND	MH4	CGND

* USB3_12P is located on the right side when facing the front panel, while USB3_34P is situated on the left side.

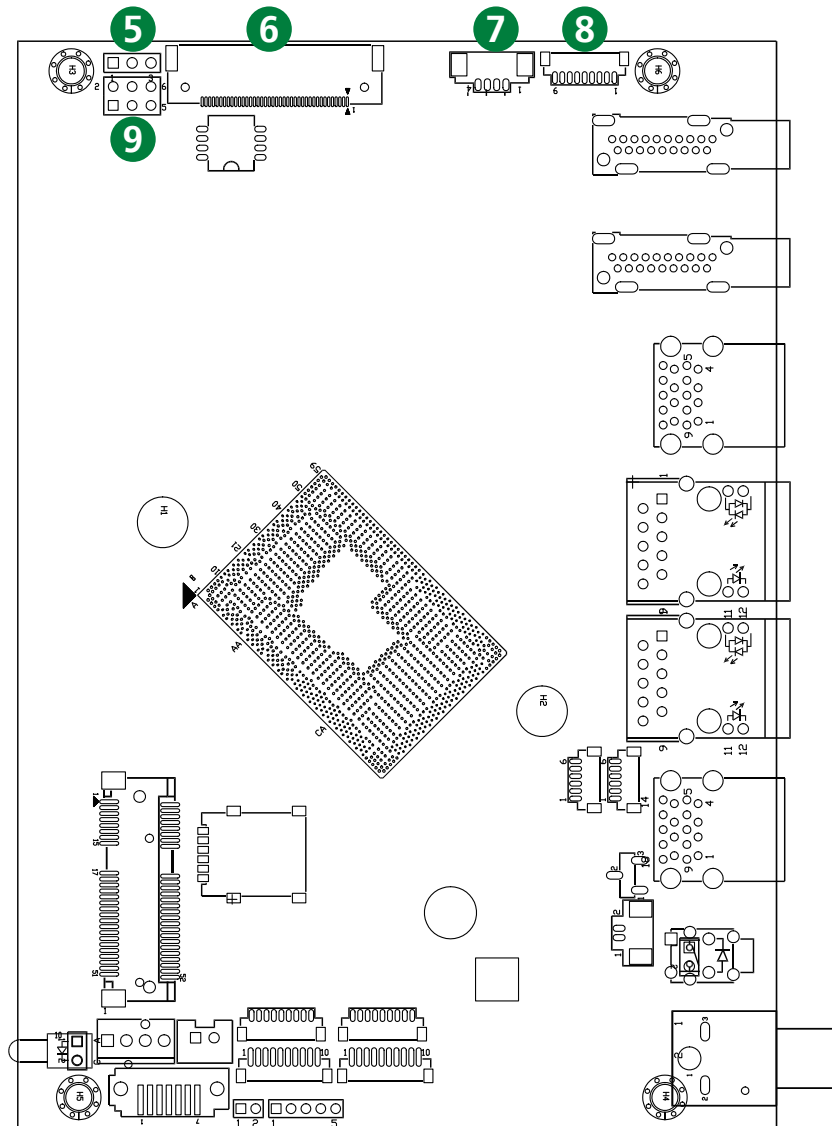
3. LAN1(3a), LAN2(3b) Connector

Pin	Definition	Pin	Definition
1	LAN1_MDI0P	2	LAN1_MDI0N
3	LAN1_MDI1P	4	LAN1_MDI2P
5	LAN1_MDI2N	6	LAN1_MDI1N
7	LAN1_MDI3P	8	LAN1_MDI3N
9	LAN1_LED_LINK100#	10	LAN1_LED_LINK2500#
11	LAN1_LED_ACT#	12	LAN1_LED_ACT_POWER
MH1	CGND	MH2	CGND

4. DCIN1

Pin	Definition
1	+12V
2	GND
3	GND

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5. LCD_PWR: LCD Panel Voltage Select

Pin	Function	Setting
1-2	+3.3V	Default
2-3	+5V	

6. LVDS_EDP: LVDS or eDP Panel Connector

Pin	LVDS Definition	Pin	eDP Definition
1	LVDS_DAT3P	1	N.C.
2	LVDS_DAT3N	2	N.C.
3	LVDS_DAT2P	3	EDP_TX0P
4	LVDS_DAT2N	4	EDP_TX0N
5	LVDS_DAT1P	5	EDP_TX1P
6	LVDS_DAT1N	6	EDP_TX1N
7	LVDS_DAT0P	7	EDP_HPDP
8	LVDS_DAT0N	8	N.C.
9	LVDS_DAT7P	9	N.C.
10	LVDS_DAT7N	10	N.C.
11	LVDS_DAT6P	11	N.C.
12	LVDS_DAT6N	12	N.C.
13	LVDS_DAT5P	13	N.C.
14	LVDS_DAT5N	14	N.C.
15	LVDS_DAT4P	15	N.C.
16	LVDS_DAT4N	16	N.C.
17	GND	17	GND
18	+V_PANEL	18	+V_PANEL
19	+V_PANEL	19	+V_PANEL
20	+V_PANEL	20	+V_PANEL
21	GND	21	GND
22	+V_PANEL	22	+V_PANEL
23	GND	23	GND
24	GND	24	GND
25	GND	25	GND
26	LVDS_CLK1P	26	EDP_AUXP
27	LVDS_CLK1N	27	EDP_AUXN
28	GND	28	GND
29	GND	29	GND
30	GND	30	GND
31	Hot-Plug Detect	31	Hot-Plug Detect
32	INV_BKLTEN	32	INV_BKLTEN
33	INV_BKLTCTRL	33	INV_BKLTCTRL
34	LVDS_CLK2P	34	N.C.
35	LVDS_CLK2N	35	N.C.
36	+V_INV	36	+V_INV
37	+V_INV	37	+V_INV
38	+V_INV	38	+V_INV
39	+V_INV	39	+V_INV
40	N.C.	40	GND

7. SPK: Speaker Connector

Pin	Definition
1	L_OUT+
2	L_OUT-
3	R_OUT+
4	R_OUT-

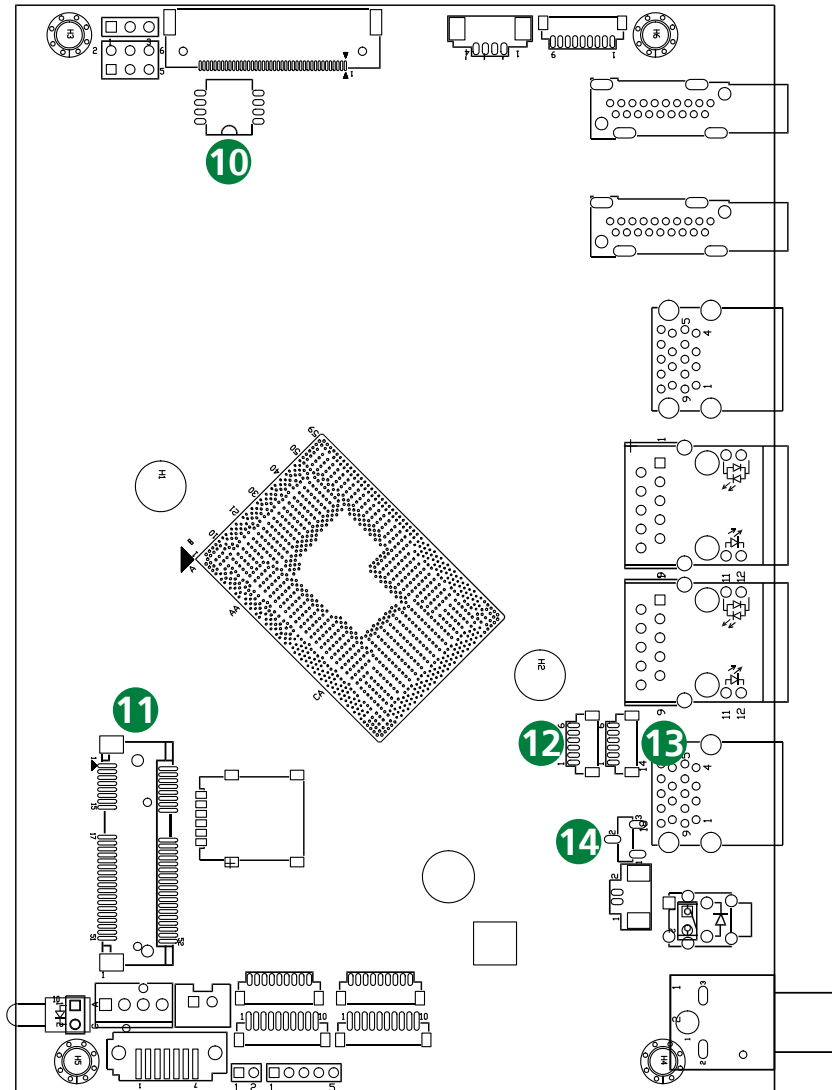
8. MIC_LOUT: MIC-In LINE-Out Connector

Pin	Definition
1	LINE_OUT-R
2	LINE_JD
3	AUDGND
4	LINE_OUT-L
5	AUDGND
6	MIC_OUT-R
7	MIC_JD
8	MIC_OUT-L
9	AUDGND

9. INV: LCD Panel Backlight Inverter Control

Pin	Definition
1	GND
2	GND
3	+V_INV
4	+V_INV
5	INV_BKLTEN
6	INV_BKLTCTRL

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10. SW1: LVDS/eDP Panel EDID Select

	SW1 Position				LVDS Panel			Setting
	Pin4	Pin3	Pin2	Pin1	Resolution	Channel	Color Depth	
LVDS	ON	ON	ON	ON	800x480	Single	6 bit	
	ON	ON	ON	OFF	1024x768	Single	6 bit	
	ON	ON	OFF	ON	1024x768	Single	8 bit	
	ON	ON	OFF	OFF	1280x768	Single	6 bit	
	ON	OFF	ON	ON	1280x800	Single	6 bit	
	ON	OFF	ON	OFF	1280x800	Single	8 bit	
	ON	OFF	OFF	ON	1280x1024	Dual	8 bit	
	ON	OFF	OFF	OFF	1366x768	Single	6 bit	
	OFF	ON	ON	ON	1366x768	Single	8 bit	
	OFF	ON	ON	OFF	1440x900	Dual	8 bit	
	OFF	ON	OFF	ON	1400x1050	Dual	8 bit	
	OFF	ON	OFF	OFF	1600x900	Dual	8 bit	
	OFF	OFF	ON	ON	1680x1050	Dual	8 bit	
	OFF	OFF	ON	OFF	1600x1200	Dual	8 bit	
OFF	OFF	OFF	ON	1920x1080	Dual	8 bit		
eDP	OFF	OFF	OFF	OFF	1920x1080	Auto		Default

11. MINI PCIe Card: PCIe Only

Pin	Definition	Pin	Definition
1	WAKE#	2	3.3V
3	NC	4	GND
5	NC	6	1.5V
7	CLKREQ#	8	UIM_PWR
9	GND	10	UIM_DATA
11	CLKN0	12	UIM_CLK
13	CLKP0	14	UIM_RESET
15	GND	16	UIM_VPP
	NC	18	GND
17	NC	18	GND
19	NC	20	W_DIS#
21	GND	22	RESET#
23	PCIE5_RXN / SATA_RXP	24	3.3V
25	PCIE5_RXP / SATA_RXN	26	GND
27	GND	28	1.5V
29	GND	30	SMB_CLK
31	PCIE5_TXN / SATA_TXN	32	SMB_DATA
33	PCIE5_TXP / SATA_TXP	34	GND
35	GND	36	USB2_6DN
37	GND	38	USB2_6DP
39	3.3V	40	GND
41	3.3V	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	1.5V
49	NC	50	GND
51	mSATA Presece Detection	52	3.3V

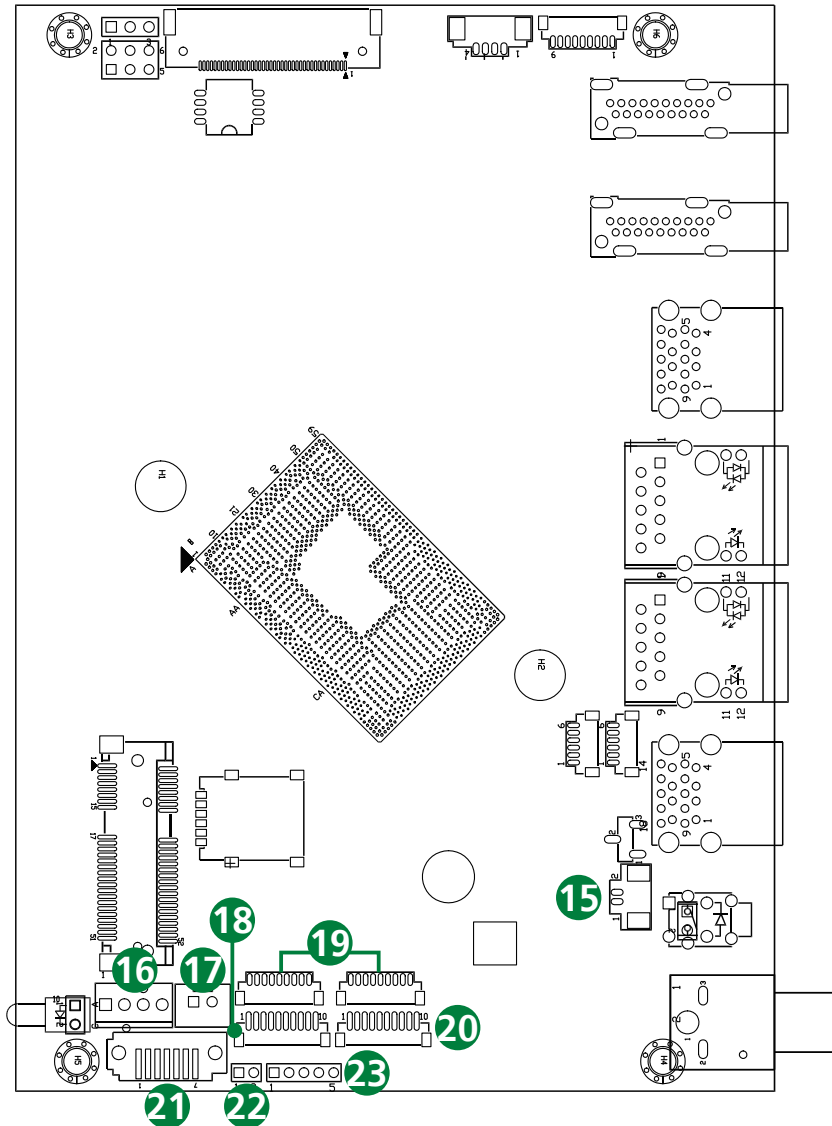
12. USB2_57: USB2.0 x2 Port

Pin	Definition
1	GND
2	USB5N
3	USB5P
4	USB7N
5	USB7P
6	+5V

13. USB2_3P: USB2.0 x2 Port

Pin	Definition
1	GND
2	NC
3	NC
4	USB3N
5	USB3P
6	+5V

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15. BAT: Battery Connector

Pin	Definition
1	GND
2	BAT

16. CPU Fan: FAN Connector

Pin	Definition
1	GND
2	+12V
3	FAN SPEED DETECT
4	FAN SPEED CONTROL

17. SATA_PWR1: SATA Power Connector

Pin	Definition
1	+5V
2	GND

18. DEBUG: 80 Debug Port

Pin	Definition
1	GND
2	PLTRST#
3	ESPI_CLK
4	ESPI_CS#
5	ESPI_IO3
6	ESPI_IO2
7	ESPI_IO1
8	ESPI_IO0
9	ESPI_RST#
10	3.3V

19. COM1, COM2: COM Port

Pin	Definition
1	RI
2	CTS
3	RTS
4	DSR
5	GND
6	DTR
7	TXD
8	RXD
9	DCD

20. GPIO: GPIO Connector

Pin	Definition
1	+5V
2	GND
3	GPO0
4	GPO1
5	GPO2
6	GPO3
7	GPI0
8	GPI1
9	GPI2
10	GPI3

21. SATA1: SATA Connector

Pin	Definition
1	GND
2	SATA_TXP
3	SATA_TXN
4	GND
5	SATA_RXN
6	SATA_RXP
7	GND

22. RESET_BTN: Reset Button

Pin	Definition
1	GND
7	RESET#

23. JP1: COM2 RI Select

Pin	Function
1-2	COM2 RI = Ring (Default)
2-3	COM2 RI = +5V
4-5	COM2 RI = +12V

