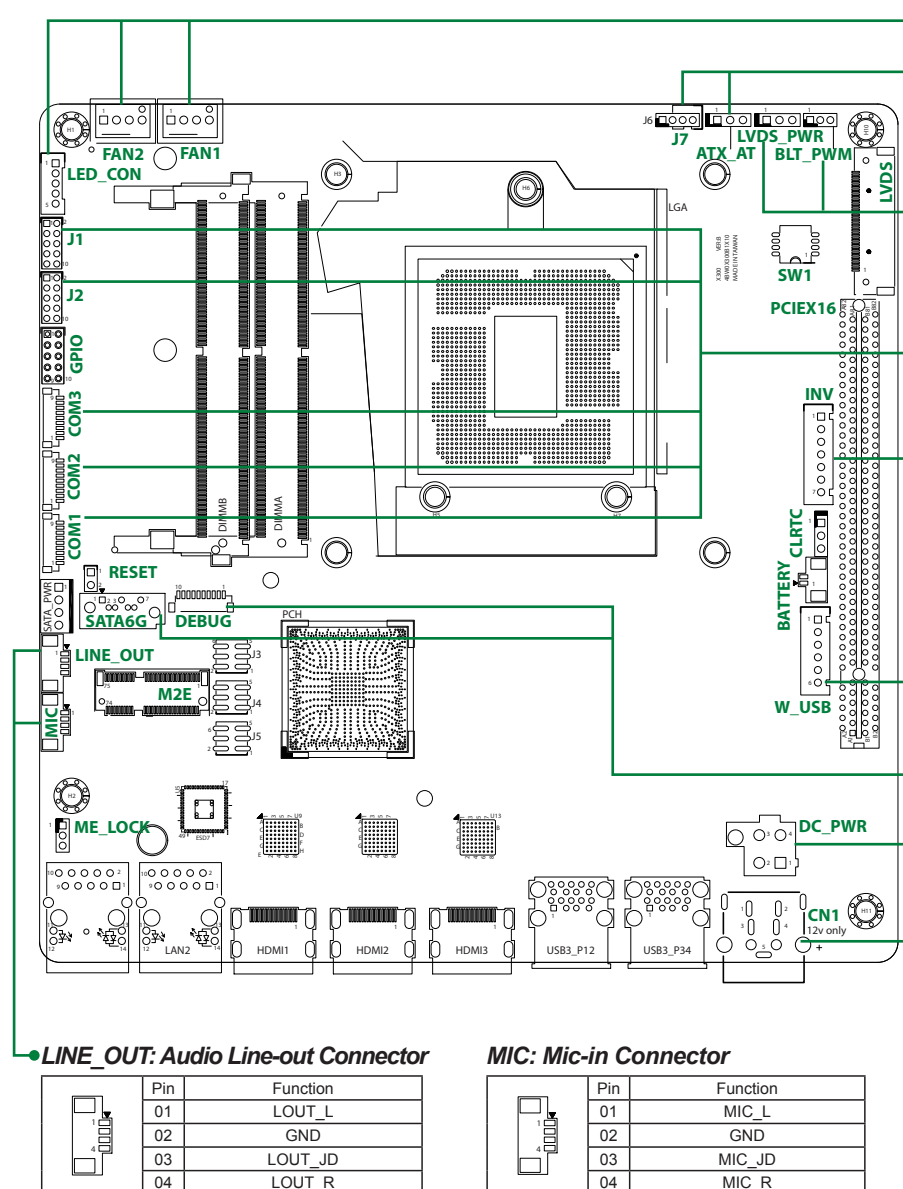


Neu-X300-H310/Q370 Quick Reference Guide

Ver A.



LED_CON: LED Connector

Pin	Function	Pin	Function
01	HDD_LED-	04	Standby LED
02	HDD_LED+	05	PWR LED
03	GND		

J7: System Power Button

Pin	Function	Pin	Function
01	PWRBTN#	02	GND

LVDS_PWR (H310 Only): LVDS Panel Voltage Select

Pin	Function	Pin	Function
1-2*	3.3V	2-3	5V

J1/J2: Internal USB 2.0 x 2 Port

Pin	Function	Pin	Function
01	5V	02	GND
03	USB1N	04	GND
05	USB1P	06	USB2P
07	GND	08	USB2N
09	GND	10	5V

INV (H310 Only): LVDS Invertor Connector

Pin	Function	Pin	Function
01	5V	05	GND
02	12V	06	GND
03	12V	07	Back light Enable
04	Back light Control		

SATA6G: SATA Connector

Pin	Function	Pin	Function
01	GND	05	SATA_RXN
02	SATA_TXP	06	SATA_RXP
03	SATA_TXN	07	GND
04	GND		

DC_PWR: ATX 4 Pin Connector

Pin	Function	Pin	Function
01	+12VSUS	03	GND
02	+12VSUS	04	GND

FAN1/FAN2: FAN Connector

Pin	Function	Pin	Function
01	GND	03	FAN Speed Detect
02	+12V	04	FAN Speed Control

ATX_AT: ATX/AT Mode Select

Pin	Function	Pin	Function
1-2*	ATX	2-3	AT

BLT_PWM (H310 Only): Back Light Control Voltage (Dimming) Voltage Select

Pin	Function	Pin	Function
1-2*	3.3V	2-3	5V

COM1/COM2/COM3: COM Port Connector

Pin	Function	Pin	Function
01	DCD	06	DSR
02	RXD	07	RTS
03	TXD	08	CTS
04	DTR	09	RI
05	GND		

W_USB: USB Touch Connector

Pin	Function	Pin	Function
01	5V	04	USB2N
02	USB1N	05	USB2P
03	USB1P	06	GND

DEBUG: DEBUG Port/LPC Bus Connector

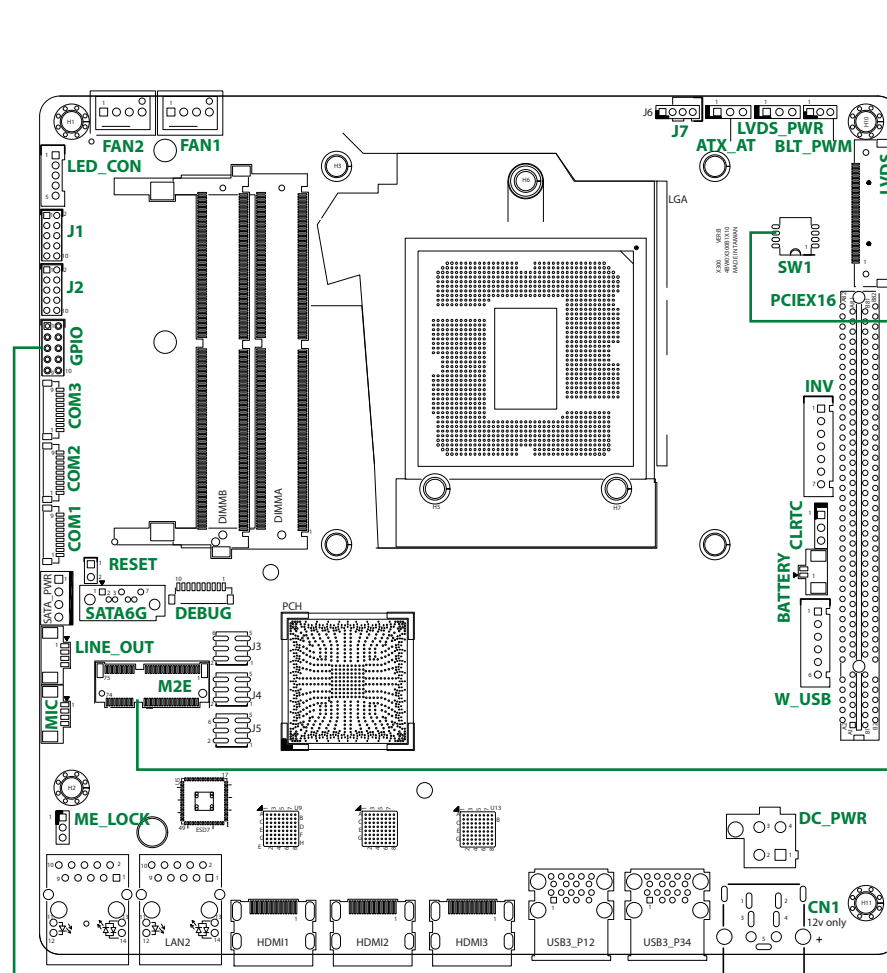
Pin	Function	Pin	Function
01	+3V	06	LAD3
02	SERIRQ	07	LFRAME#
03	LAD0	08	24MHZ
04	LAD1	09	PLTRST#
05	LAD2	10	GND

CN1: DC Power Input Jack (+12V Only)

Pin	Function	Pin	Function
01	+12VSUS	04	GND
02	+12VSUS	05	CGND
03	GND		

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GPIO: GPIO Connector

Pin	Function	Pin	Function
01	+5V	02	GND
03	SIO_GP10	04	SIO_GP11
05	SIO_GP12	06	SIO_GP13
07	SIO_GP14	08	SIO_GP15
09	SIO_GP16	10	SIO_GP17

LVDS (H310 Only): LVDS Panel Connector

Pin	Function	Pin	Function	Pin	Function	Pin	Function
01	LVDS0_D3+	11	LVDS1_D2+	21	GND	31	SMBUS_CLK
02	LVDS0_D3-	12	LVDS1_D2-	22	3.3V	32	Back Light Enable
03	LVDS0_D2+	13	LVDS1_D1+	23	GND	33	Back Light Control
04	LVDS0_D2-	14	LVDS1_D1-	24	GND	34	LVDS1_CLK+
05	LVDS0_D1+	15	LVDS1_D0+	25	GND	35	LVDS1_CLK-
06	LVDS0_D1-	16	LVDS1_D0-	26	LVDS0_CLK+	36	+12V
07	LVDS0_D0+	17	GND	27	LVDS0_CLK-	37	+12V
08	LVDS0_D0-	18	+V_PANEL (3.3V or 5V)	28	GND	38	+12V
09	LVDS1_D3+	19	+V_PANEL (3.3V or 5V)	29	GND	39	NC
10	LVDS1_D3-	20	+V_PANEL (3.3V or 5V)	30	GND	40	SMBUS_DAT

SW1: LVDS Resolution Select

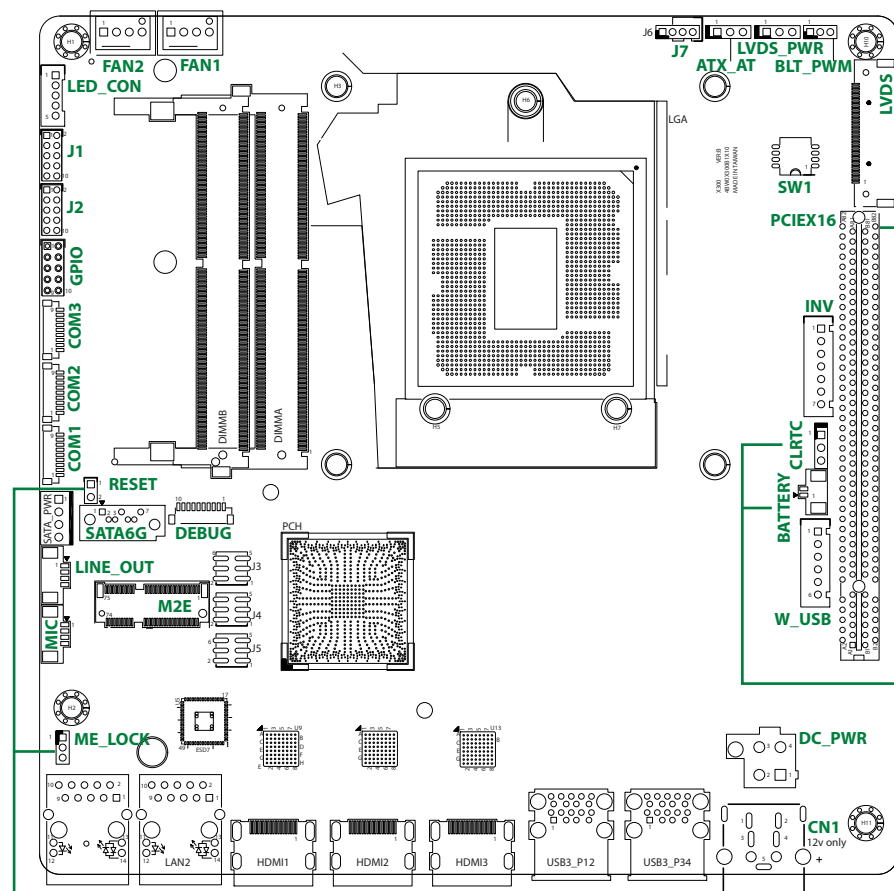
GPIO[3:0]	SW1[3]	SW1[2]	SW1[1]	SW1[0]	HA	VA	CD (bit)	port
0000	ON	ON	ON	ON	800	600	6	single
0001	ON	ON	ON	OFF	1024	768	6	single
0010	ON	ON	OFF	ON	1024	768	8	single
0011	ON	ON	OFF	OFF	1280	1024	6	single
0100	ON	OFF	ON	ON	1280	800	6	single
0101	ON	OFF	ON	OFF	1280	960	6	single
0110	ON	OFF	OFF	ON	1280	1024	8	dual
0111	ON	OFF	OFF	OFF	1366	768	6	single
1000	OFF	ON	ON	ON	1366	768	8	single
1001	OFF	ON	ON	OFF	1440	900	8	dual
1010	OFF	ON	OFF	ON	1400	1050	8	dual
1011	OFF	ON	OFF	OFF	1600	900	8	dual
1100	OFF	OFF	ON	ON	1680	1050	8	dual
1101	OFF	OFF	ON	OFF	1600	1200	8	dual
1110	OFF	OFF	OFF	ON	1920	1080	8	dual
1111	OFF	OFF	OFF	OFF	1920	1200	8	dual

M2E (2230_M2E)

Pin	Function	Pin	Function	Pin	Function	Pin	Function
01	GND	18	NC	43	PCIE_RXN	60	NC
02	3VSB	19	NC	44	NC	61	NC
03	USBP	20	NC	45	GND	62	NC
04	3VSB	21	NC	46	NC	63	GND
05	USBN	22	NC	47	CLK_PCIEP	64	NC
06	NC	23	NC	48	NC	65	NC
07	GND	32	NC	49	CLK_PCIEN	66	NC
08	NC	33	GND	50	SUSCLK	67	NC
09	NC	34	NC	51	GND	68	NC
10	NC	35	PCIE_TXP	52	PLTRST#	69	GND
11	NC	36	NC	53	CLKREQ#	70	NC
12	NC	37	PCIE_TXN	54	BT_DISABLE#	71	NC
13	NC	38	NC	55	WAKE#	72	3VSB
14	NC	39	GND	56	WIFI_DISABLE#	73	NC
15	NC	40	NC	57	GND	74	3VSB
16	NC	41	PCIE_RXP	58	NC	75	GND
17	NC	42	NC	59	NC		

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•RESET: System Reset Button

Pin	Function
01	RESET#
02	GND

ME_LOCK: ME Flash Descriptor Security Override

Pin	Function
1-2*	Lock
2-3	un-Lock

•PCIEX16 (Q370 Only): PCIe x16 Slot

Pin	Function	Pin	Function	Pin	Function	Pin	Function
A1	GND	A42	GND	B1	+12V	B42	TXN6
A2	+12V	A43	RXP6	B2	+12V	B43	GND
A3	+12V	A44	RXN6	B3	+12V	B44	GND
A4	GND	A45	GND	B4	GND	B45	TXP7
A5	NC	A46	GND	B5	SMBCLK	B46	TXN7
A6	NC	A47	RXP7	B6	SMBDAT	B47	GND
A7	NC	A48	RXN7	B7	GND	B48	NC
A8	NC	A49	GND	B8	+3.3V	B49	GND
A9	+3.3V	A50	NC	B9	NC	B50	TXP8
A10	+3.3V	A51	GND	B10	+3VSUS	B51	TXN8
A11	PCIRST#	A52	RXP8	B11	WAKE#	B52	GND
A12	GND	A53	RXN8	B12	NC	B53	GND
A13	CLK_PCIEP	A54	GND	B13	GND	B54	TXP9
A14	CLK_PCIEN	A55	GND	B14	TXP0	B55	TXN9
A15	GND	A56	RXP9	B15	TXN0	B56	GND
A16	RXP0	A57	RXN9	B16	GND	B57	GND
A17	RXN0	A58	GND	B17	PCIEX16_PRSENT#	B58	TXP10
A18	GND	A59	GND	B18	GND	B59	TXN10
A19	NC	A60	RXP10	B19	TXP1	B60	GND
A20	GND	A61	RXN10	B20	TXN1	B61	GND
A21	RXP1	A62	GND	B21	GND	B62	TXP11
A22	RXN1	A63	GND	B22	GND	B63	TXN11
A23	GND	A64	RXP11	B23	TXP2	B64	GND
A24	GND	A65	RXN11	B24	TXN2	B65	GND
A25	RXP2	A66	GND	B25	GND	B66	TXP12
A26	RXN2	A67	GND	B26	GND	B67	TXN12
A27	GND	A68	RXP12	B27	TXP3	B68	GND
A28	GND	A69	RXN12	B28	TXN3	B69	GND
A29	RXP3	A70	GND	B29	GND	B70	TXP13
A30	RXN3	A71	GND	B30	NC	B71	TXN13
A31	GND	A72	RXP13	B31	NC	B72	GND
A32	NC	A73	RXN13	B32	GND	B73	GND
A33	NC	A74	GND	B33	TXP4	B74	TXP14
A34	GND	A75	GND	B34	TXN4	B75	TXN14
A35	RXP4	A76	RXP14	B35	GND	B76	GND
A36	RXN4	A77	RXN14	B36	GND	B77	GND
A37	GND	A78	GND	B37	TXP5	B78	TXP15
A38	GND	A79	GND	B38	TXN5	B79	TXN15
A39	RXP5	A80	RXP15	B39	GND	B80	GND
A40	RXN5	A81	RXN15	B40	GND	B81	NC
A41	GND	A82	GND	B41	TXP6	B82	NC

•CLRTC: Clear CMOS

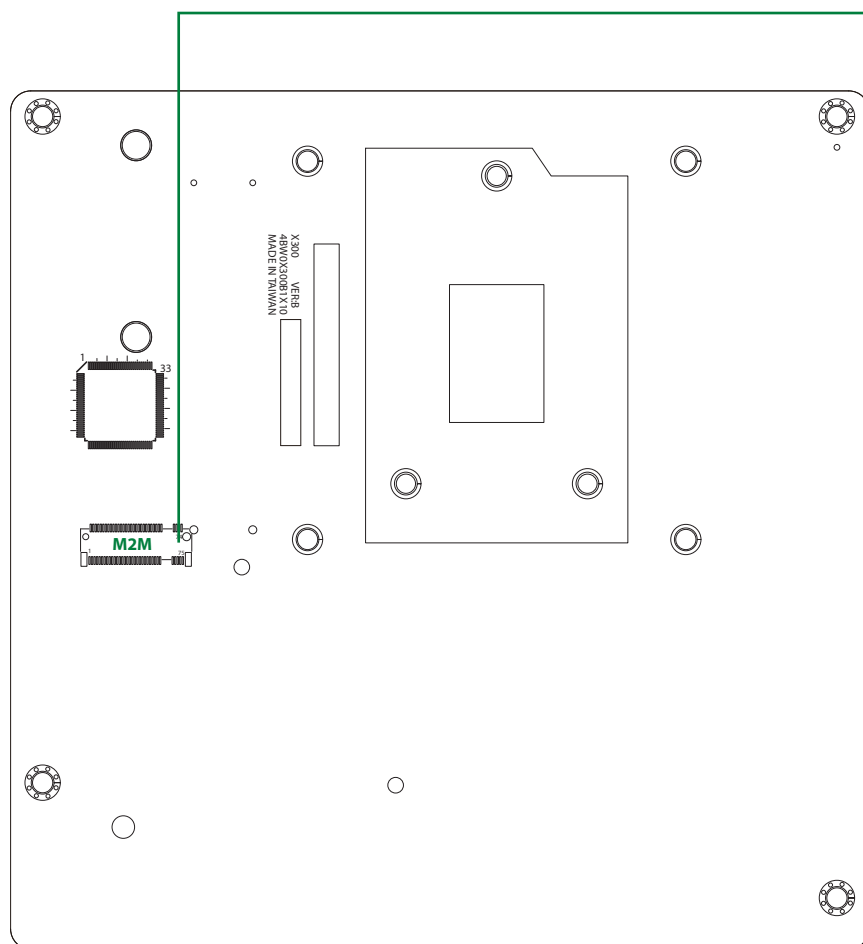
Pin	Function
1-2*	Normal
2-3	Clear CMOS

BATTERY: Battery Connector

Pin	Function
01	BAT
02	GND

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M2M (2280_2242_M2M)

Pin	Function	Pin	Function	Pin	Function	Pin	Function
01	GND	18	+3VSB	35	PCIE2_RXN	52	CLKREQ#
02	+3VSB	19	PCIE3_RXP	36	NC	53	CLK_PCIEN
03	GND	20	NC	37	PCIE2_RXP	54	WAKE#
04	+3VSB	21	GND	38	DEVSLP	55	CLK_PCIEP
05	PCIE4_RXN	22	NC	39	GND	56	NC
06	NC	23	PCIE3_RXN	40	NC	57	GND
07	PCIE4_RXP	24	NC	41	SATA_RXP(PCIE1_RXP)	58	NC
08	NC	25	PCIE3_RXP	42	NC	67	NC
09	GND	26	NC	43	SATA_RXN(PCIE1_RXN)	68	NC
10	M2M_LED#	27	GND	44	NC	69	M2M_PEDET
11	PCIE4_RXN	28	NC	45	GND	70	+3VSB
12	+3VSB	29	PCIE2_RXN	46	NC	71	GND
13	PCIE4_RXP	30	NC	47	SATA_TXN(PCIE1_TXN)	72	+3VSB
14	+3VSB	31	PCIE2_RXP	48	NC	73	GND
15	GND	32	NC	49	SATA_TXP(PCIE1_TXP)	74	+3VSB
16	+3VSB	33	GND	50	RESET#	75	GND
17	PCIE3_RXN	34	NC	51	GND		

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□=Pin1 [*]=Default setting