

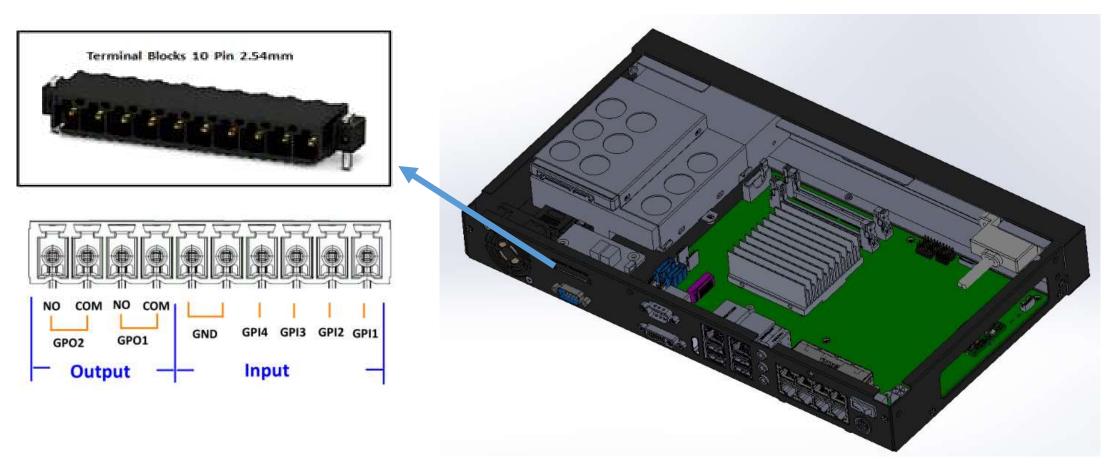
## NViS1482/14162 GPIO Design Proposal

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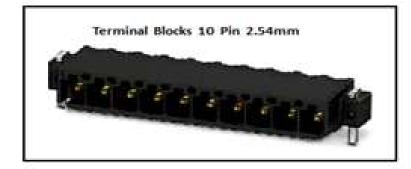
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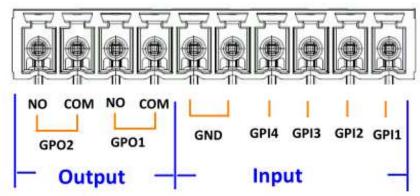
## NViS1482/14162 4IN/20UT GPIO Design



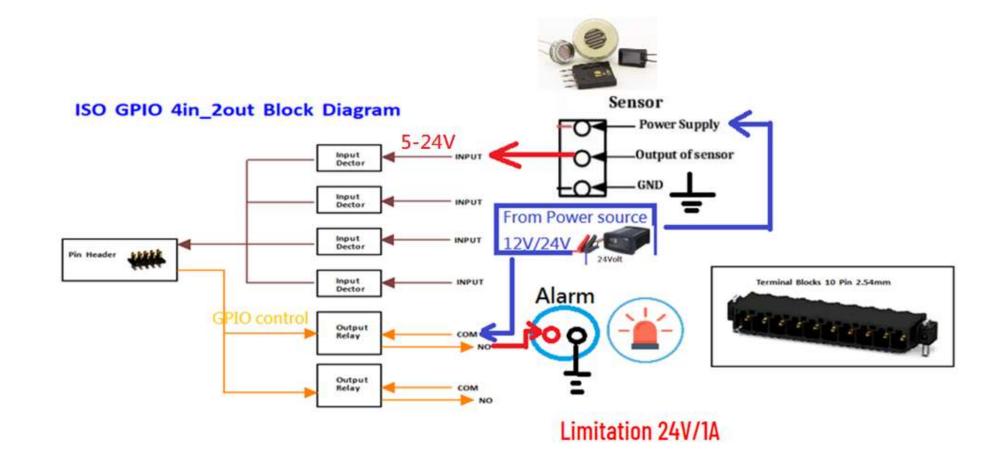
### NViS1482/14162 4IN/20UT Isolated GPI0 Design



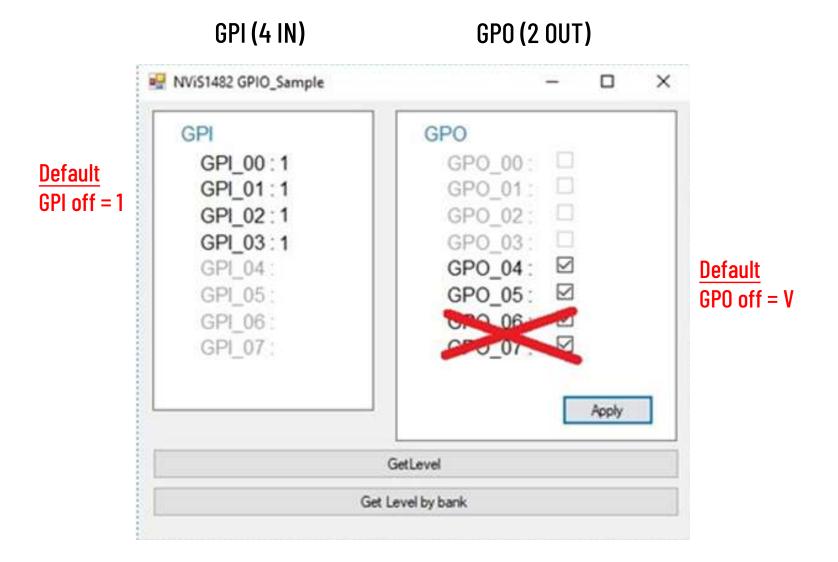




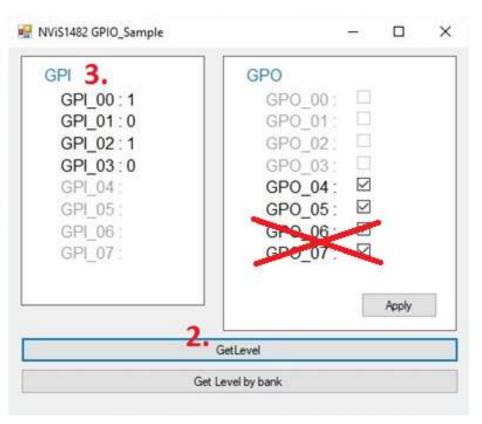
#### NViS1482/14162 4IN/20UT Isolated GPI0 Design Application



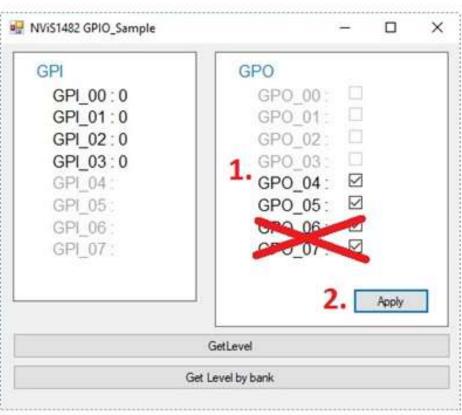
#### NViS1482 4IN/20UT SDK Control Initial Status (Windows)



### NViS1482 4IN/20UT SDK Control (Windows)



#### GPI (4IN)



#### GPO (20UT)



# Thank You

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