

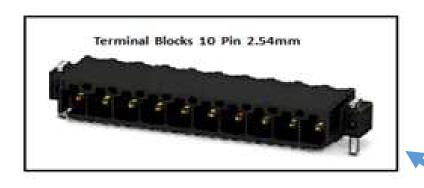
NViS1482/14162 GPIO Design Proposal

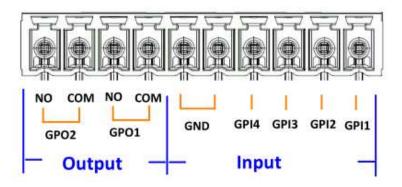
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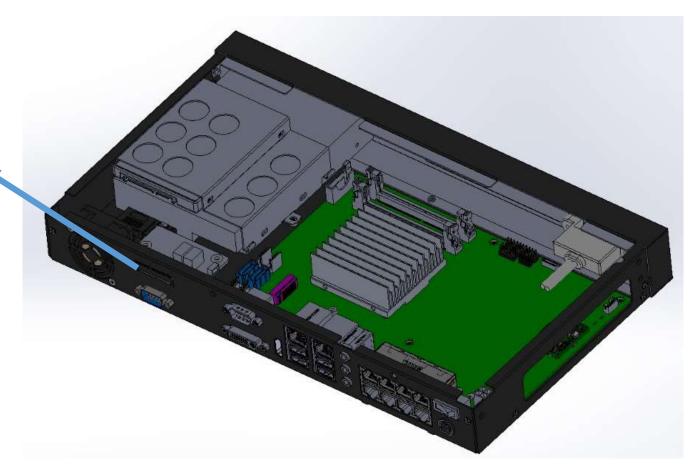




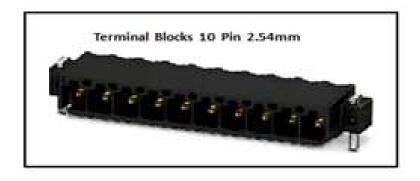
NViS1482/14162 4IN/20UT GPIO Design



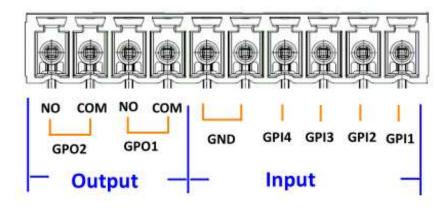




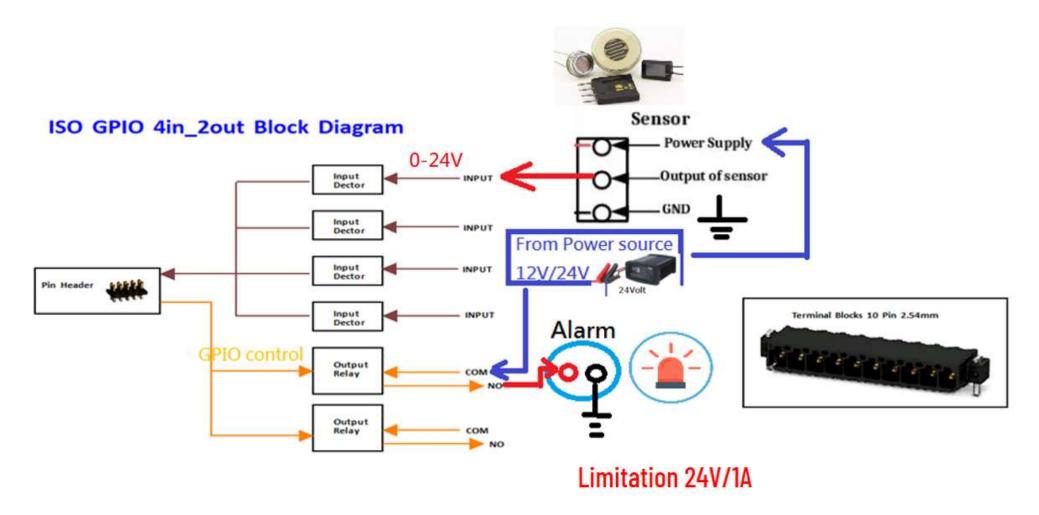
NViS1482/14162 4IN/20UT Isolated GPIO Design



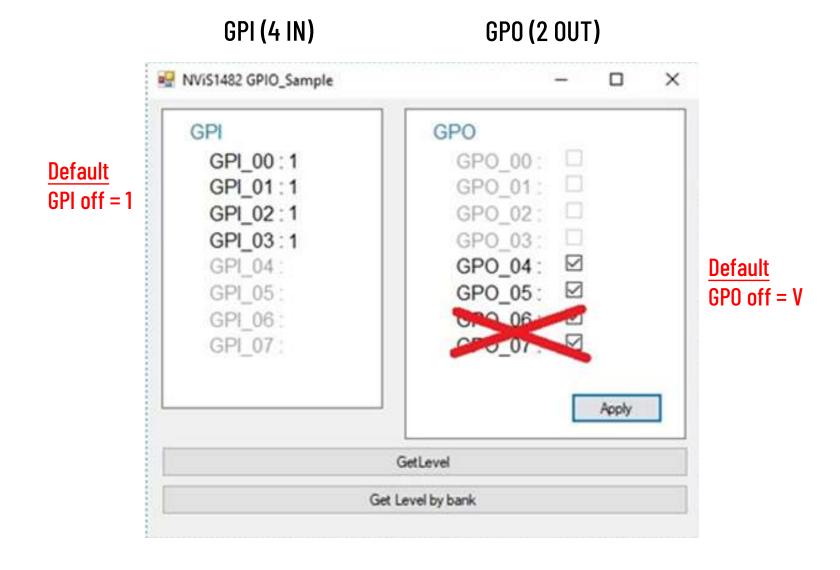




NViS1482/14162 4IN/20UT Isolated GPIO Design Application

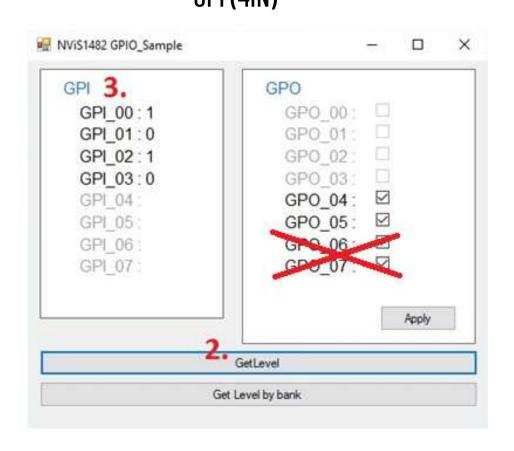


NViS1482 4IN/20UT SDK Control Initial Status (Windows)



NViS1482 4IN/20UT SDK Control (Windows)

GPI (4IN) GPO (20UT)



P NViS1482 GPIO_Sample × **GPI GPO** GPI 00:0 GPO 00: GPI_01:0 GPO 01: GPI 02:0 GPO 02: GPO 03: GPI 03:0 GPO 04: GPI_04: GPO 05: GPI 05: GPI 06: GPI 07: Apply GetLevel Get Level by bank



Thank You

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