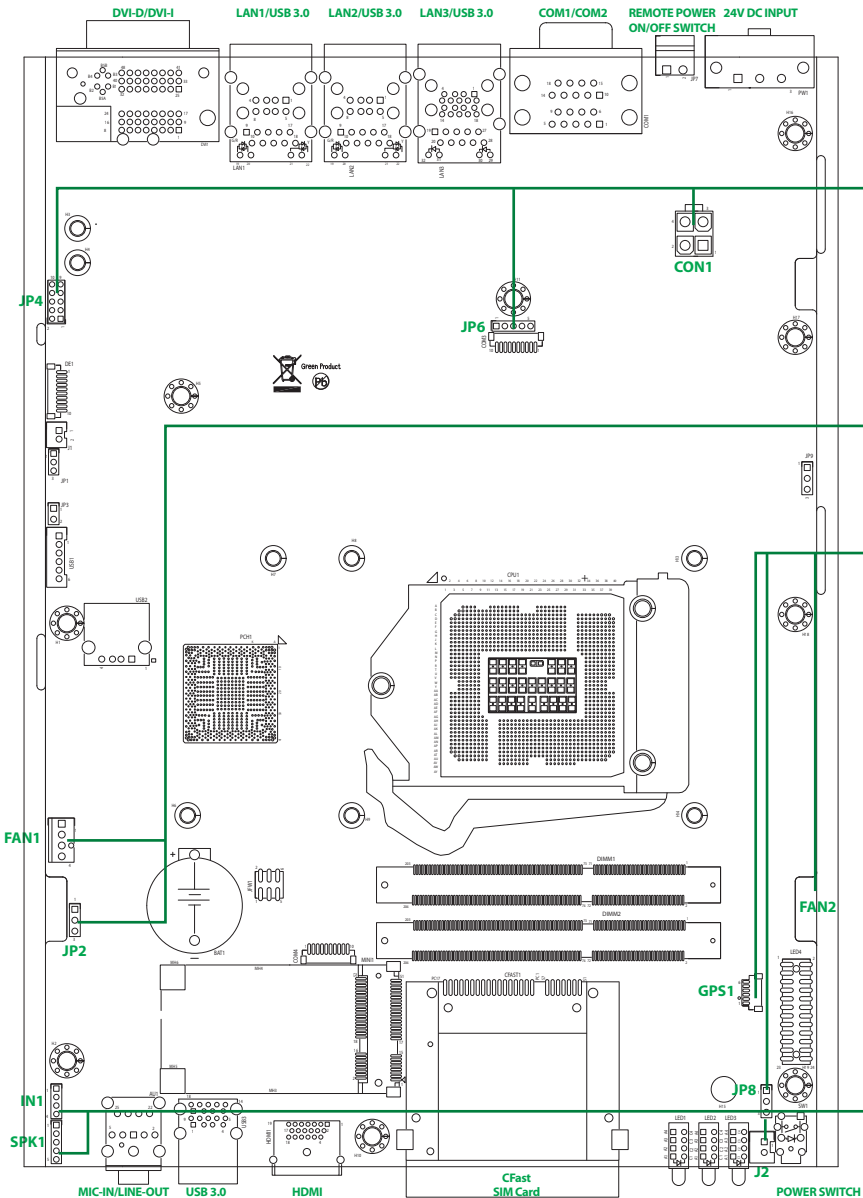


# NISE 3700 Quick Reference Guide

Ver A. (P/N: 60177A0450X00)



## JP4: GPIO PIN HEADER

Pin	Description	Pin	Description
1	VCC5	2	GND
3	GPI60	4	GPO64
5	GPI61	6	GPO65
7	GPI62	8	GPO66
9	GPI63	10	GPO67

## FAN1: SYSTEM FAN CONNECTOR

Pin	Definition	Pin	Definition
1	GND	2	+12V
3	FAN TAC	4	FAN CTL

## GPS1: GPS PIN HEADER

Pin	Description	Pin	Description
1	GPS_BAT	2	GPS_LED
3	UART_TXD4	4	UART_RXD4
5	GND	6	VCC3
MH1	GND	MH1	GND

## IN1: LINE IN CONNECTOR

Pin	Definition
1	LINE1-L1
2	AGND
3	LINEIN_JD
4	LINE1-R1

## JP6: COM3 RI PIN HEADER

Pin No.	Status	Function Description
1-2	Short	VCC5
2-3	Short	VCC12
4-5 (*)	Short	RING

## JP2: CMOS CLEAR PIN HEADER

Pin No.	Status	Function Description
1-2 (*)	Short	Normal
2-3	Short	Clear BIOS Setting

## JP8: AT/ATX MODE SELECT

Pin No.	Status	Function Description
1-2 (*)	Short	ATX Mode
2-3	Short	AT Mode

## SPK1: SPEAK OUT CONNECTOR

Pin	Definition	Pin	Definition
1	OUT-LR+_C	2	OUT-LR-_C
3	AGND	4	OUT-RR+_C
5	OUT-RR-_C		

## CN1: DC IN POWER CONNECTOR

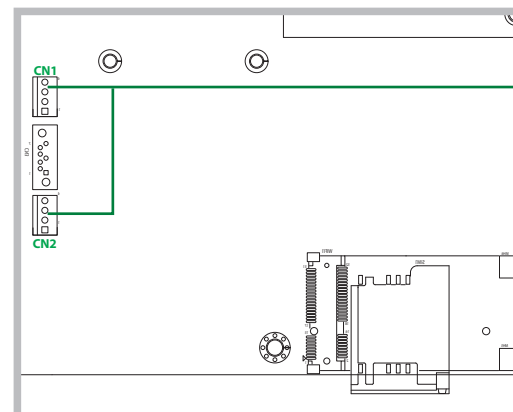
Pin	Definition
1	GND
2	GND
3	VIN_3
4	VIN_3

## FAN2: SYSTEM FAN CONNECTOR

Pin	Definition
1	GND
2	+12V
3	FAN TAC
4	FAN CTL

## J2: RESET JST CONNECTOR

Pin	Definition
1	PM_RESET#_J
2	GND



## CN1/CN2: SATA POWER CONNECTOR

Pin	Definition	Pin	Definition
1	VCC12	2	GND
3	GND	4	VCC5

Quick reference guide is subject to change without prior notice.  
For any update, please visit our website:  
[www.nexcom.com](http://www.nexcom.com)

□ = Pin1 [\*] = Default setting