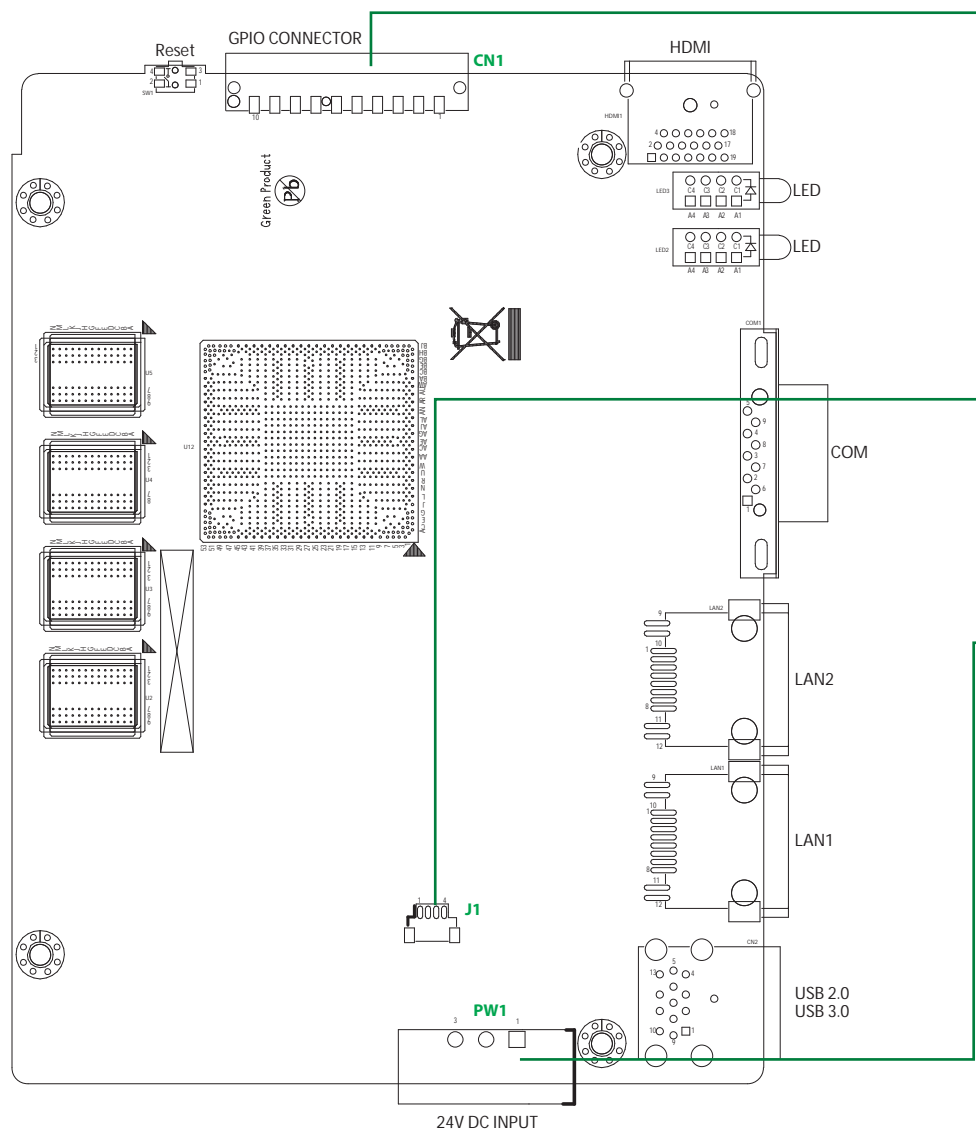


NIFE104 Quick Reference Guide

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CN1: GPIO Connector

Pin	Description
01	VCC5
02	GND
03	ICH_GPIO_IN
04	ICH_GPI1_IN
05	ICH_GPI2_IN
06	ICH_GPI3_IN
07	ICH_GPO0_OUT
08	ICH_GPO1_OUT
09	ICH_GPO2_OUT
10	ICH_GPO3_OUT

J1: MCU FW Connector

Pin	Description
01	GND
02	SBW_TDIO
03	SBW_TCK
04	3VSB

PW1: Power Connector

Pin	Description
01	+24v
02	GND
03	PE

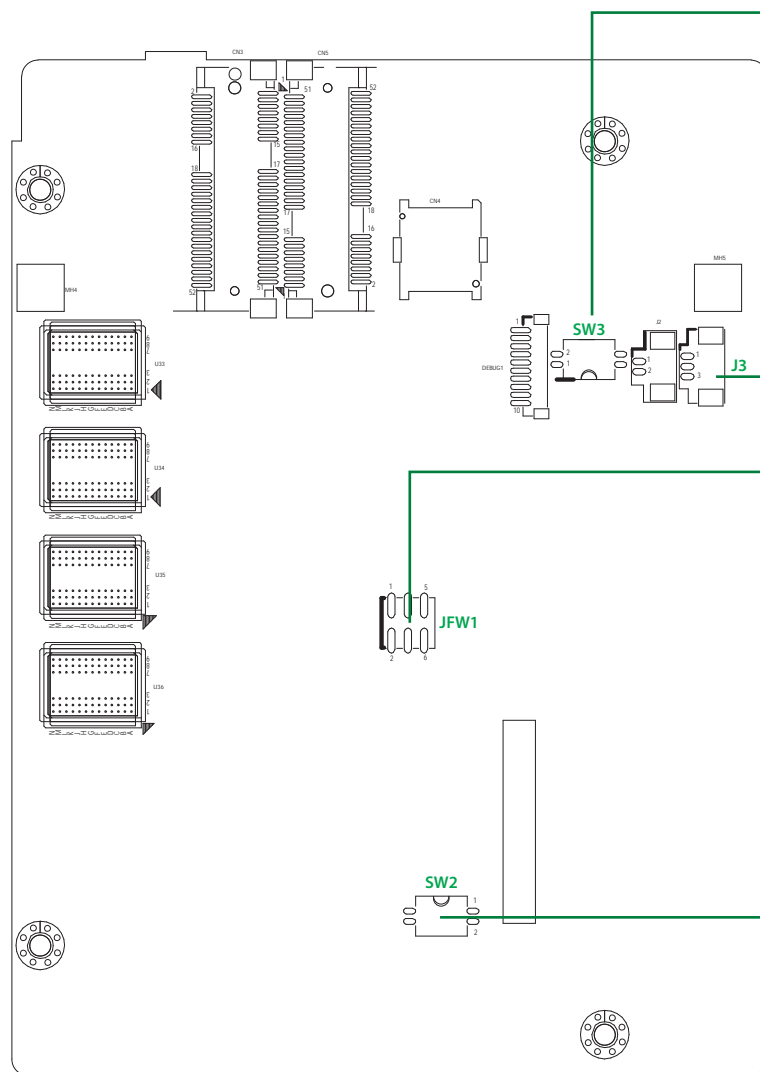
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= Pin1 [*] = Default setting



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SW3: Clear CMOS Connector

Pin	Description
01	RTC_TEST#
02	SRTC_RST#
03	GND
04	GND

Pin	Description
1-2(*)	Normal
3-4	Clear BIOS Setting

J3: Remote Switch/S3 Connector

Pin	Description
01	GND
02	EC_PWRBT#
03	S3_OUT

JFW1: BIOS Flash Pin Header

Pin	Description
01	BIOS_SPI_CS#0
02	BIOS_SPI_SO
03	VSPI
04	GND
05	BIOS_SPI_SI
06	BIOS_SPI_CLK
07	BIOS_HOLD#
08	VSPI

SW2: AT/ATX Switch

Pin	Description
01	AT_ATX_SELECT
02	NC
03	GND
04	GND

Pin	Description
1-2	ATX MODE
3-4(*)	AT MODE

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= Pin1 [*] = Default setting

