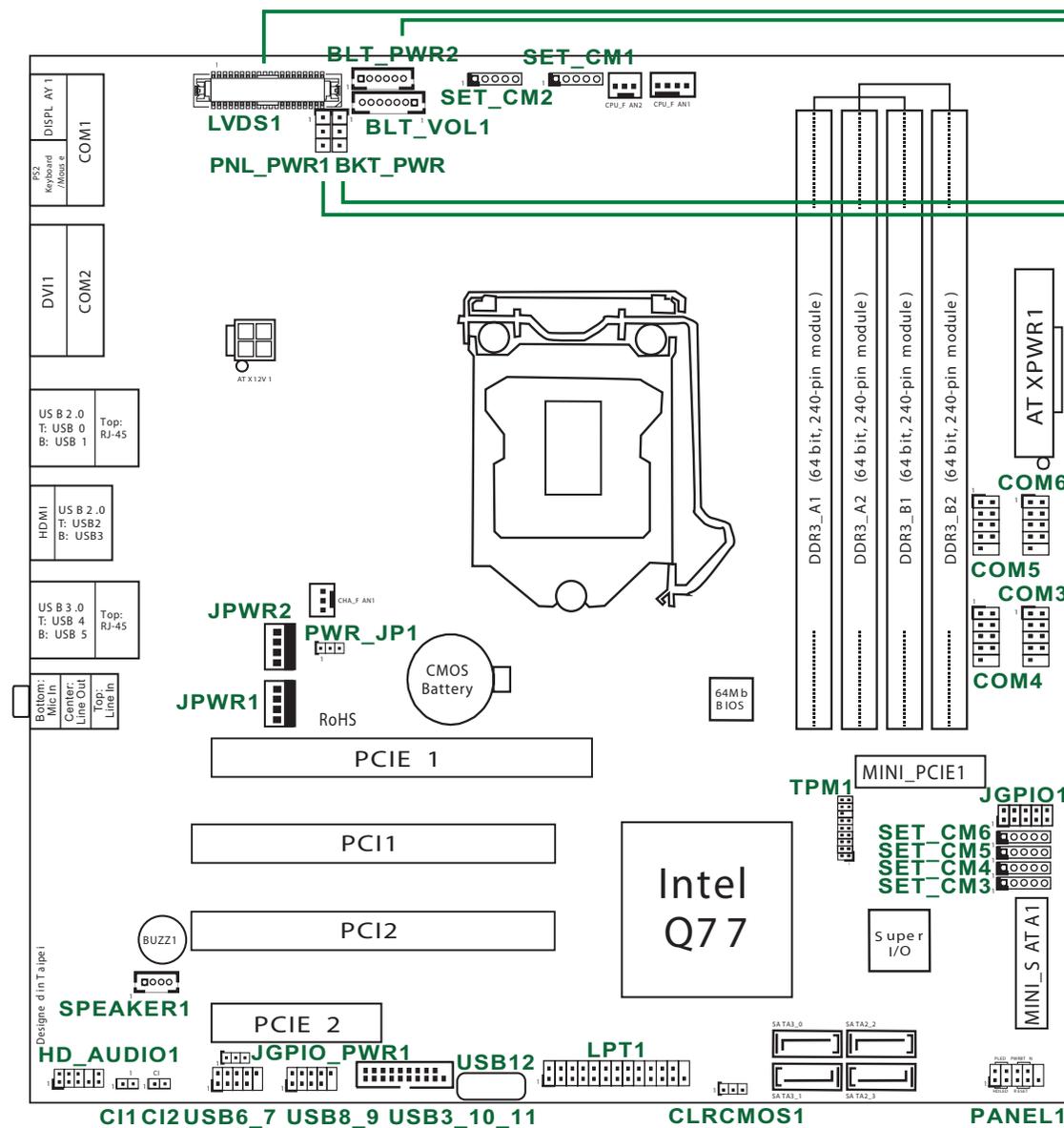
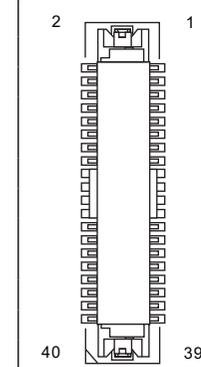


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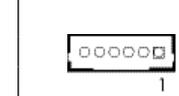


1. LVDS Panel Connector



| Pin | Signal | Pin | Signal |
|-----|-----------------|-----|----------------|
| 1 | R_LVDD | 2 | R_LVDD |
| 3 | +3V | 4 | LDDC_CLK |
| 5 | LDDC_DATA | 6 | LVDS_A_DATA0# |
| 7 | LVDS_A_DATA0 | 8 | GND |
| 9 | LVDS_A_DATA1# | 10 | LVDS_A_DATA1 |
| 11 | GND | 12 | LVDS_A_DATA2# |
| 13 | LVDS_A_DATA2 | 14 | GND |
| 15 | LVDS_A_DATA3# | 16 | LVDS_A_DATA3 |
| 17 | GND | 18 | LVDS_A_CLK# |
| 19 | LVDS_A_CLK | 20 | GND |
| 21 | LVDS_B_DATA0# | 22 | LVDS_B_DATA0 |
| 23 | GND | 24 | LVDS_B_DATA1# |
| 25 | LVDS_B_DATA1 | 26 | GND |
| 27 | LVDS_B_DATA2# | 28 | LVDS_B_DATA2 |
| 29 | DPLVDD_EN | 30 | LVDS_B_DATA3# |
| 31 | LVDS_B_DATA3 | 32 | GND |
| 33 | LVDS_B_CLK# | 34 | LVDS_B_CLK |
| 35 | GND | 36 | CON_LBKLT_EN_R |
| 37 | CON_LBKLT_CTL_R | 38 | LCD_BLT_VCC |
| 39 | LCD_BLT_VCC | 40 | LCD_BLT_VCC |

2. Inverter Power Control Header



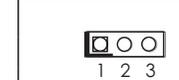
| Pin | Signal | Pin | Signal |
|-----|---------------|-----|--------------|
| 1 | GND | 2 | GND |
| 3 | CON_LBKLT_CTL | 4 | CON_LBKLT_EN |
| 5 | LCD BLT VCC | 6 | LCD BLT VCC |

3. Backlight Power Jumper



| Pin | Signal | Pin | Signal |
|-----|--------|-----|--------|
| 1 | +5V | 2 | +5V |
| 2 | +12V | 3 | +12V |

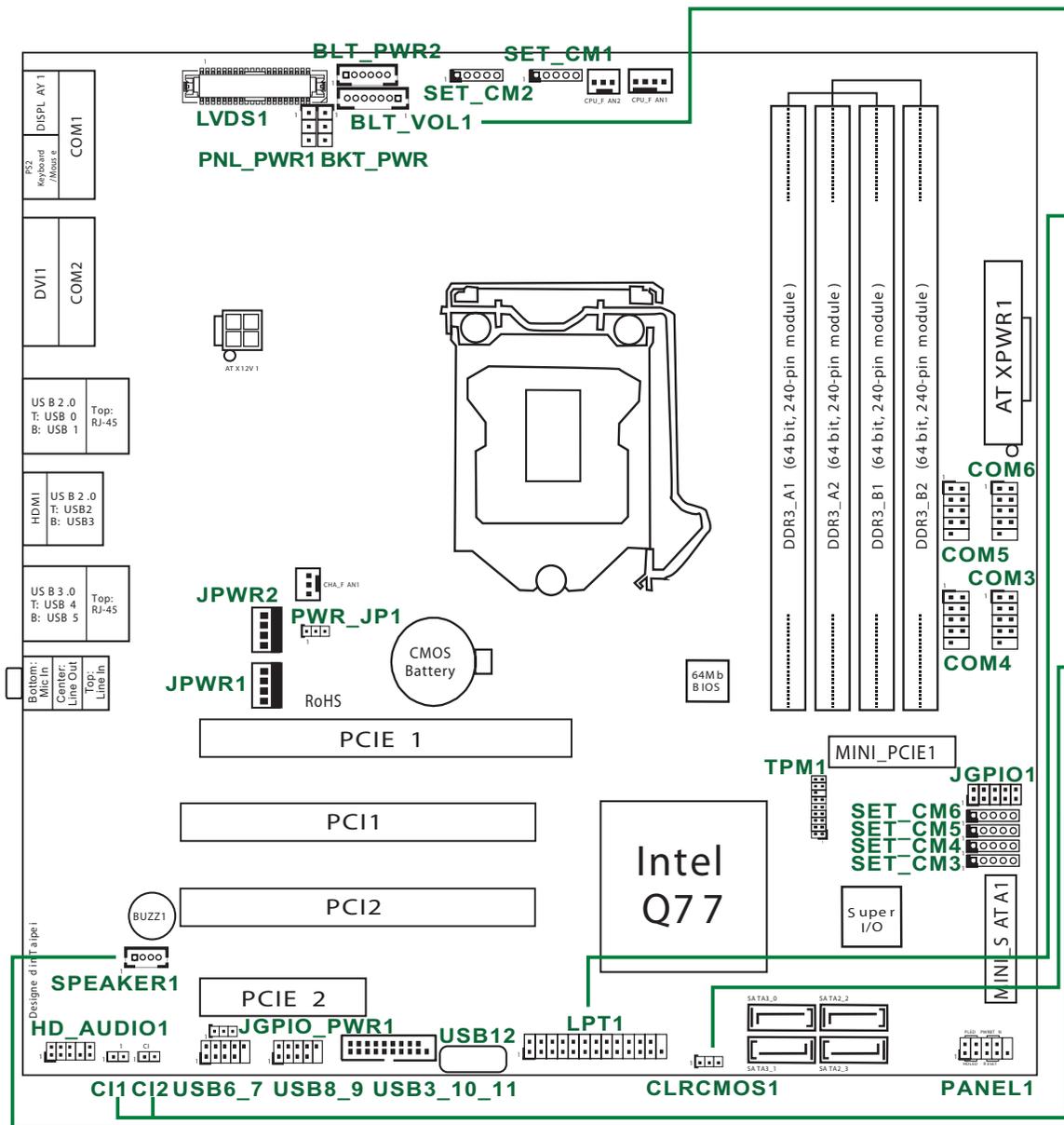
4. Panel Power Jumper



| Pin | Signal | Pin | Signal |
|-----|--------|-----|--------|
| 1 | +3V | 2 | +3V |
| 2 | +5V | 3 | +5V |

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5. Backlight & Amp Volume Control Header

| Pin | | Signal | Pin | | Signal |
|-----|--|-------------|-----|--|-------------|
| 1 | | GPIO_VOL_UP | 2 | | GPIO_VOL_DW |
| 3 | | PWRDN | 4 | | BKT_UP |
| 5 | | GPIO_BLT_UP | 6 | | GPIO_BLT_DW |
| 7 | | GND | | | |

6. Printer Port Header

| Pin | | Signal | Pin | | Signal |
|-----|--|--------|-----|--|--------|
| 1 | | STB# | 2 | | SPD0 |
| 3 | | SPD1 | 4 | | SPD2 |
| 5 | | SPD3 | 6 | | SPD4 |
| 7 | | SPD5 | 8 | | SPD6 |
| 9 | | SPD7 | 10 | | ACK# |
| 11 | | BUSY | 12 | | PE |
| 13 | | SLCT | 14 | | AFD# |
| 15 | | ERROR# | 16 | | PINIT# |
| 17 | | SLIN# | 18 | | GND |
| 19 | | GND | 20 | | GND |
| 21 | | GND | 22 | | GND |
| 23 | | GND | 24 | | GND |
| 25 | | GND | | | |

7. Clear CMOS Header

| Pin | | Signal | Pin | | Signal |
|-----|--|------------|-----|--|------------|
| 1 | | Normal | 2 | | Normal |
| 2 | | Clear CMOS | 3 | | Clear CMOS |

8. Chassis Intrusion Header 1, 2

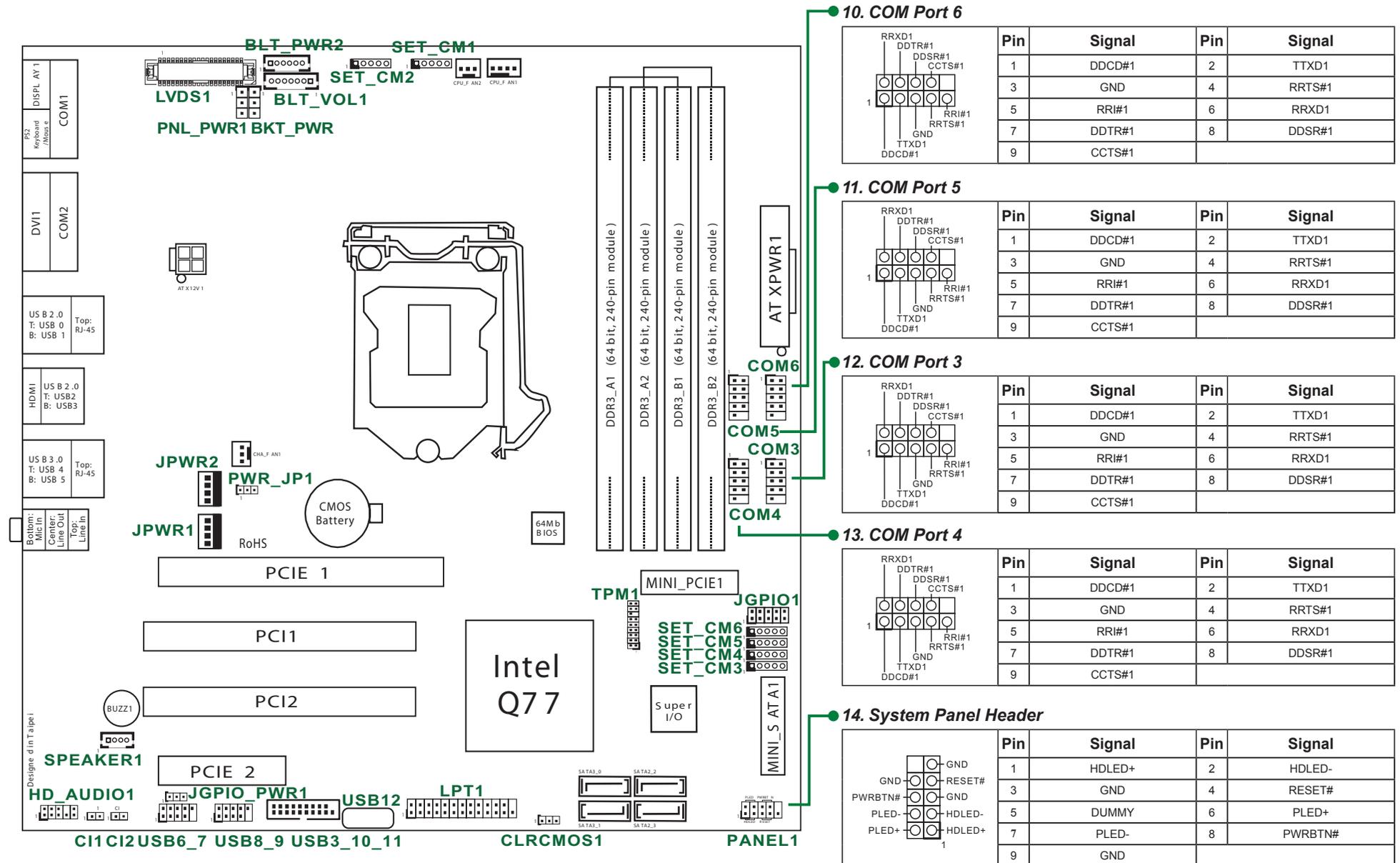
| Pin | | Signal | Pin | | Signal |
|-----|--|--------|-----|--|--------|
| 1 | | Signal | 2 | | GND |

9. 3W Audio AMP Output Wafer

| Pin | | Signal | Pin | | Signal |
|-----|--|--------|-----|--|--------|
| 1 | | SPK L- | 2 | | SPK L+ |
| 2 | | SPK R+ | 3 | | SPK R- |

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10. COM Port 6

| Pin | Signal | Pin | Signal |
|-----|--------|-----|--------|
| 1 | DDCD#1 | 2 | TTXD1 |
| 3 | GND | 4 | RRTS#1 |
| 5 | RRI#1 | 6 | RRXD1 |
| 7 | DDTR#1 | 8 | DDSR#1 |
| 9 | CCTS#1 | | |

11. COM Port 5

| Pin | Signal | Pin | Signal |
|-----|--------|-----|--------|
| 1 | DDCD#1 | 2 | TTXD1 |
| 3 | GND | 4 | RRTS#1 |
| 5 | RRI#1 | 6 | RRXD1 |
| 7 | DDTR#1 | 8 | DDSR#1 |
| 9 | CCTS#1 | | |

12. COM Port 3

| Pin | Signal | Pin | Signal |
|-----|--------|-----|--------|
| 1 | DDCD#1 | 2 | TTXD1 |
| 3 | GND | 4 | RRTS#1 |
| 5 | RRI#1 | 6 | RRXD1 |
| 7 | DDTR#1 | 8 | DDSR#1 |
| 9 | CCTS#1 | | |

13. COM Port 4

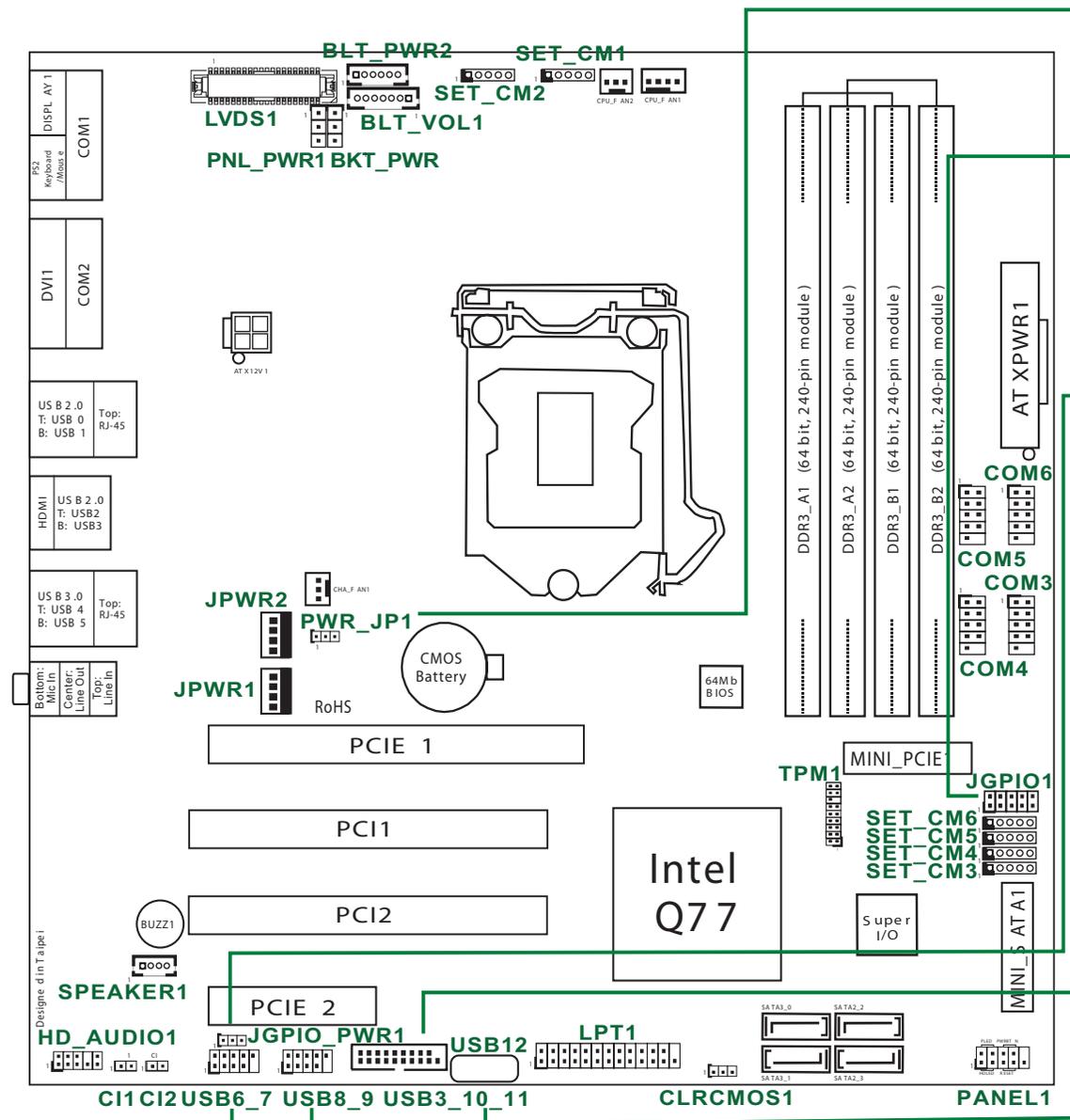
| Pin | Signal | Pin | Signal |
|-----|--------|-----|--------|
| 1 | DDCD#1 | 2 | TTXD1 |
| 3 | GND | 4 | RRTS#1 |
| 5 | RRI#1 | 6 | RRXD1 |
| 7 | DDTR#1 | 8 | DDSR#1 |
| 9 | CCTS#1 | | |

14. System Panel Header

| Pin | Signal | Pin | Signal |
|-----|--------|-----|---------|
| 1 | HDLED+ | 2 | HDLED- |
| 3 | GND | 4 | RESET# |
| 5 | DUMMY | 6 | PLED+ |
| 7 | PLED- | 8 | PWRBTN# |
| 9 | GND | | |

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21. ATX/ AT mode Selection

| | | | | |
|-------|-----|----------|-----|----------|
| | Pin | Signal | Pin | Signal |
| 1 2 3 | 1 | AT mode | 2 | AT mode |
| | 2 | ATX mode | 3 | ATX mode |

22. Digital Input/ Output Pin Header

| | | | | |
|------|-----|------------------|-----|-----------------|
| | Pin | Signal | Pin | Signal |
| 2 10 | 1 | Digital Output 0 | 2 | Digital Input 0 |
| 1 9 | 3 | Digital Output 1 | 4 | Digital Input 1 |
| | 5 | Digital Output 2 | 6 | Digital Input 2 |
| | 7 | Digital Output 3 | 8 | Digital Input 3 |
| | 9 | JGPIO_PWR1 | 10 | GND |

23. Digital Input/ Output Power select

| | | | | |
|-------|-----|--------|-----|--------|
| | Pin | Signal | Pin | Signal |
| 1 2 3 | 1 | +12V | 2 | +12V |
| | 2 | +5V | 3 | +5V |

24. USB3.0 Header (USB3_10_11)

| | | | | |
|----|---------------|--------|---------------|--------|
| | Pin | Signal | Pin | Signal |
| 1 | Dummy | 2 | IntA_PB_D+ | |
| 3 | IntA_PB_D- | 4 | GND | |
| 5 | IntA_PB_SSTX+ | 6 | IntA_PB_SSTX- | |
| 7 | GND | 8 | IntA_PB_SSRX+ | |
| 9 | IntA_PB_SSRX- | 10 | Vbus | |
| 11 | IntA_PA_D+ | 12 | IntA_PA_D- | |
| 13 | GND | 14 | IntA_PA_SSTX+ | |
| 15 | IntA_PA_SSTX- | 16 | GND | |
| 17 | IntA_PA_SSRX+ | 18 | IntA_PA_SSRX- | |
| 19 | Vbus | 20 | | |

25. USB2.0 Header (USB12, USB6_7, USB_8_9,)

| | | | | |
|---|---------|--------|-----|--------|
| | Pin | Signal | Pin | Signal |
| 1 | USB_PWR | 2 | -A | |
| 3 | +A | 4 | GND | |
| 5 | USB_PWR | 6 | -B | |
| 7 | +B | 8 | GND | |
| 9 | DUMMY | | | |

