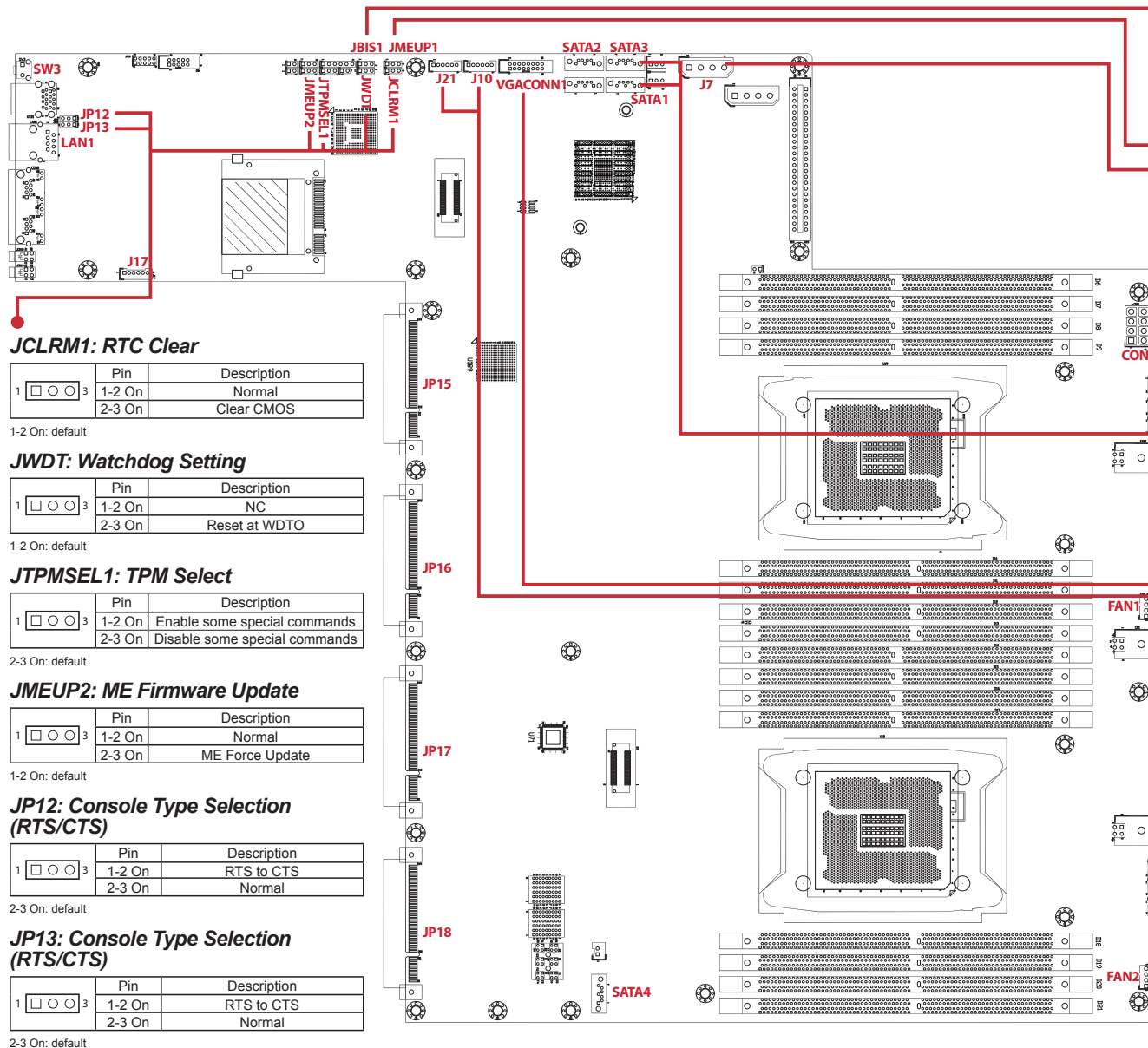


NSA 7135 Quick Reference Guide



JCLRM1: RTC Clear

Pin	Description
1-2 On	Normal
2-3 On	Clear CMOS

1-2 On: default

JWDT: Watchdog Setting

Pin	Description
1-2 On	NC
2-3 On	Reset at WDTO

1-2 On: default

JTPMSEL1: TPM Select

Pin	Description
1-2 On	Enable some special commands
2-3 On	Disable some special commands

2-3 On: default

JMEUP2: ME Firmware Update

Pin	Description
1-2 On	Normal
2-3 On	ME Force Update

1-2 On: default

JP12: Console Type Selection (RTS/CTS)

Pin	Description
1-2 On	RTS to CTS
2-3 On	Normal

2-3 On: default

JP13: Console Type Selection (RTS/CTS)

Pin	Description
1-2 On	RTS to CTS
2-3 On	Normal

2-3 On: default

JBIS1: BIOS Recovery

Pin	Description
1-2 On	Normal
2-3 On	Recover BIOS

1-2 On: default

JMEUP1: Password Clear

Pin	Description
1-2 On	Normal
2-3 On	Password Clear

1-2 On: default

J7: ATX Connector

Pin	Definition
1	12V
2	GND
3	GND
4	5V

SATA1, SATA2, SATA3, SATA4: SATA Connectors

Pin	Definition	Pin	Definition
1	GND	2	TXP
3	TXN	4	GND
5	RXN	6	RXP
7	GND		

VGACONN1: VGA Connector

Pin	Definition	Pin	Definition
1	DACROA_B	2	DACGOA_B
3	DACBOA_B	4	NC
5	GND	6	GND
7	GND	8	GND
9	VGA_VCC	10	GND
11	NC	12	DDC_DATA0_B
13	AHSYNCO_B	14	AVSYNCO_B
15	DDC_CLKO_B	16	NC

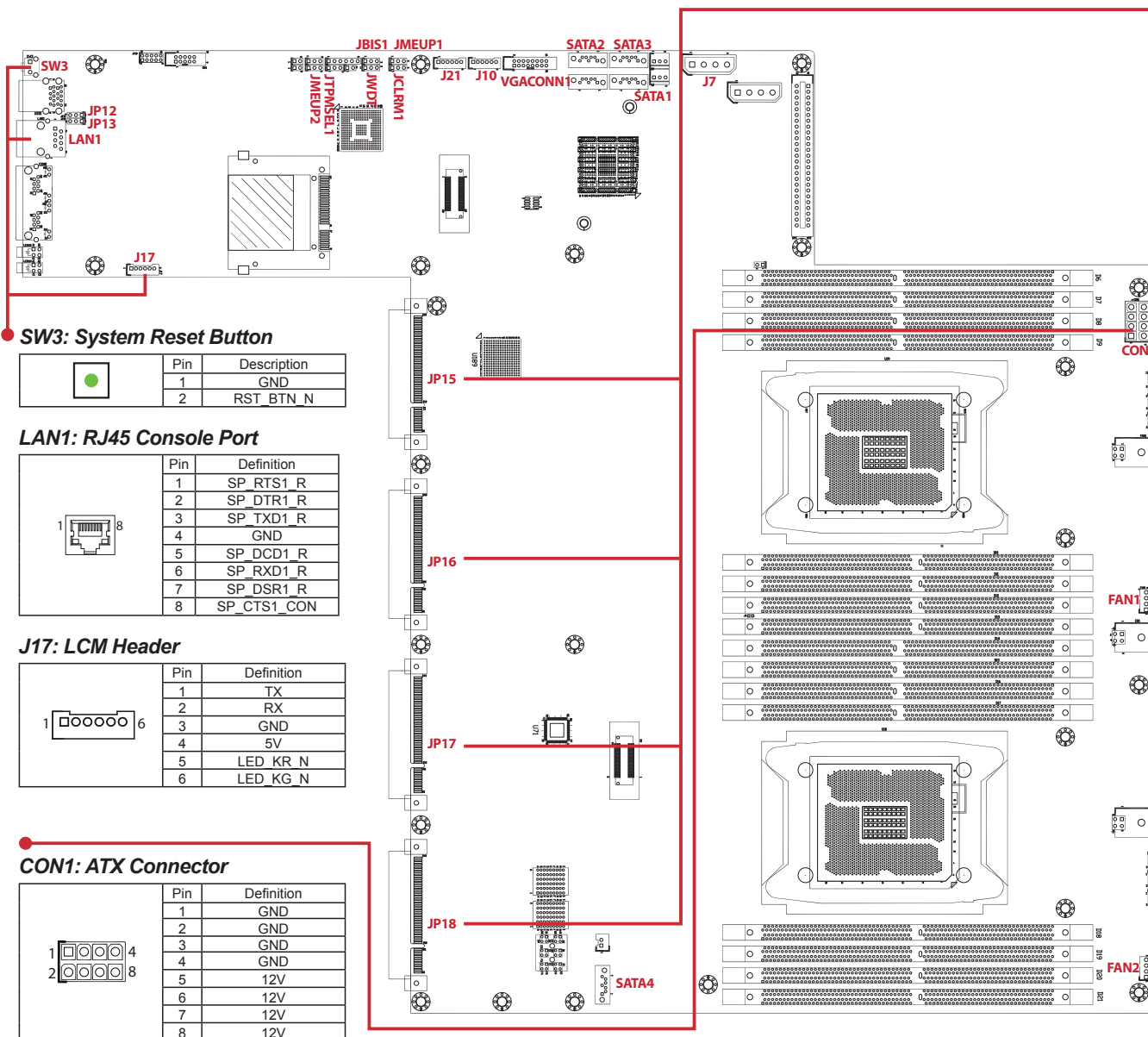
J10, J21: USB Headers

Pin	Definition	Pin	Definition
1	5V	2	DN1
3	DP1	4	DN2
5	DP2	6	GND

FAN1, FAN2: Fan Headers

Pin	Definition
1	GND
2	P12V
3	FAN TACH
4	FAN PWM

NSA 7135 Quick Reference Guide



SW3: System Reset Button

Pin	Description
1	GND
2	RST_BTN_N

LAN1: RJ45 Console Port

Pin	Definition
1	SP_RTS1_R
2	SP_DTR1_R
3	SP_TXD1_R
4	GND
5	SP_DCD1_R
6	SP_RXD1_R
7	SP_DSR1_R
8	SP_CTS1_CON

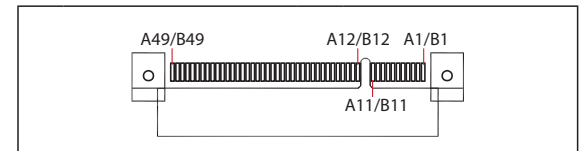
J17: LCM Header

Pin	Definition
1	TX
2	RX
3	GND
4	5V
5	LED_KR_N
6	LED_KG_N

CON1: ATX Connector

Pin	Definition
1	GND
2	GND
3	GND
4	GND
5	12V
6	12V
7	12V
8	12V

JP15, JP16, JP17, JP18: PCIe Slots



Pin	Definition	Pin	Definition
A1	GND	B1	12V
A2	12V	B2	12V
A3	12V	B3	12V
A4	GND	B4	GND
A5	NC	B5	SMBCLK
A6	NC	B6	SMBDATA
A7	NC	B7	GND
A8	NC	B8	3.3V
A9	3.3V	B9	NC
A10	3.3V	B10	P3V3AUX
A11	PRSN#	B11	WAKE#
A12	GND	B12	BYPASS_GATE Enable
A13	CLK_P	B13	GND
A14	CLK_N	B14	TX0P
A15	GND	B15	TX0N
A16	RX0P	B16	GND
A17	RX0N	B17	NC
A18	GND	B18	GND
A19	BYPASS_A Enable	B19	TX1P
A20	GND	B20	TX1N
A21	RX1P	B21	GND
A22	RX1N	B22	GND
A23	GND	B23	TX2P
A24	GND	B24	TX2N
A25	RX2P	B25	GND
A26	RX2N	B26	GND
A27	GND	B27	TX3P
A28	GND	B28	TX3N
A29	RX3P	B29	GND
A30	RX3N	B30	BYPASS_B Enable
A31	GND	B31	NC
A32	BYPASS_C Enable	B32	GND
A33	BYPASS_D Enable	B33	TX4P
A34	GND	B34	TX4N
A35	RX4P	B35	GND
A36	RX4N	B36	GND
A37	GND	B37	TX5P
A38	GND	B38	TX5N
A39	RX5P	B39	GND
A40	RX5N	B40	GND
A41	GND	B41	TX6P
A42	GND	B42	TX6N
A43	RX6P	B43	GND
A44	RX6N	B44	GND
A45	GND	B45	TX7P
A46	GND	B46	TX7N
A47	RX7P	B47	GND
A48	RX7N	B48	NC
A49	GND	B49	GND

