

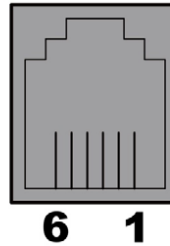
# Cash Drawer Pin-Definition

V1.1\_4/9,'11

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## Cash Drawer RJ-11 Pin Definition

Pin	Signal
1	CASEOPEN2#
2	CASH1_P
3	CASEOPEN#
4	24V
5	CASH2_P
6	GND



## Cash Drawer Programming Guide

RJ-11 Pin	Signal	IO map
1	CASEOPEN2#	Bit3
2	CASH1_P	Bit0
3	CASEOPEN#	Bit2
4	24V	
5	CASH2_P	Bit1
6	GND	

### Cash Drawer Programming Sample Code

```
#define Superio_Port    0x2E
void Superio_Enter_Config(void){
    outportb(Superio_Port, 0x87);
    outportb(Superio_Port, 0x87);
}

void Superio_Exit_Config(void){
    outportb(Superio_Port, 0xAA);
}

BYTE Superio_Get_Reg(BYTE Reg){
    outportb(Superio_Port, Reg);
    return inportb(Superio_Port+1);
}

void Superio_Set_Reg(BYTE Reg,BYTE Data){
    outportb(Superio_Port, Reg);
    outportb(Superio_Port+1, Data);
}

void Superio_Set_Logic_Device(BYTE Logic_Device){
    Superio_Set_Reg(7,Logic_Device);
}

void main(void)
{
    BYTE value;
```

```
Superio_Enter_Config();
Superio_Set_Logic_Device(7);

//init GPIO
value = Superio_Get_Reg(0x0F0);
value &= ~0x0F;
value |= 0xC0;
Superio_Set_Reg(0x0F0, value);

//set CASH1_P = HI
Superio_Set_Reg(0x0F1, Superio_Get_Reg(0x0F1)|0x01);

Superio_Exit_Config();
}
```