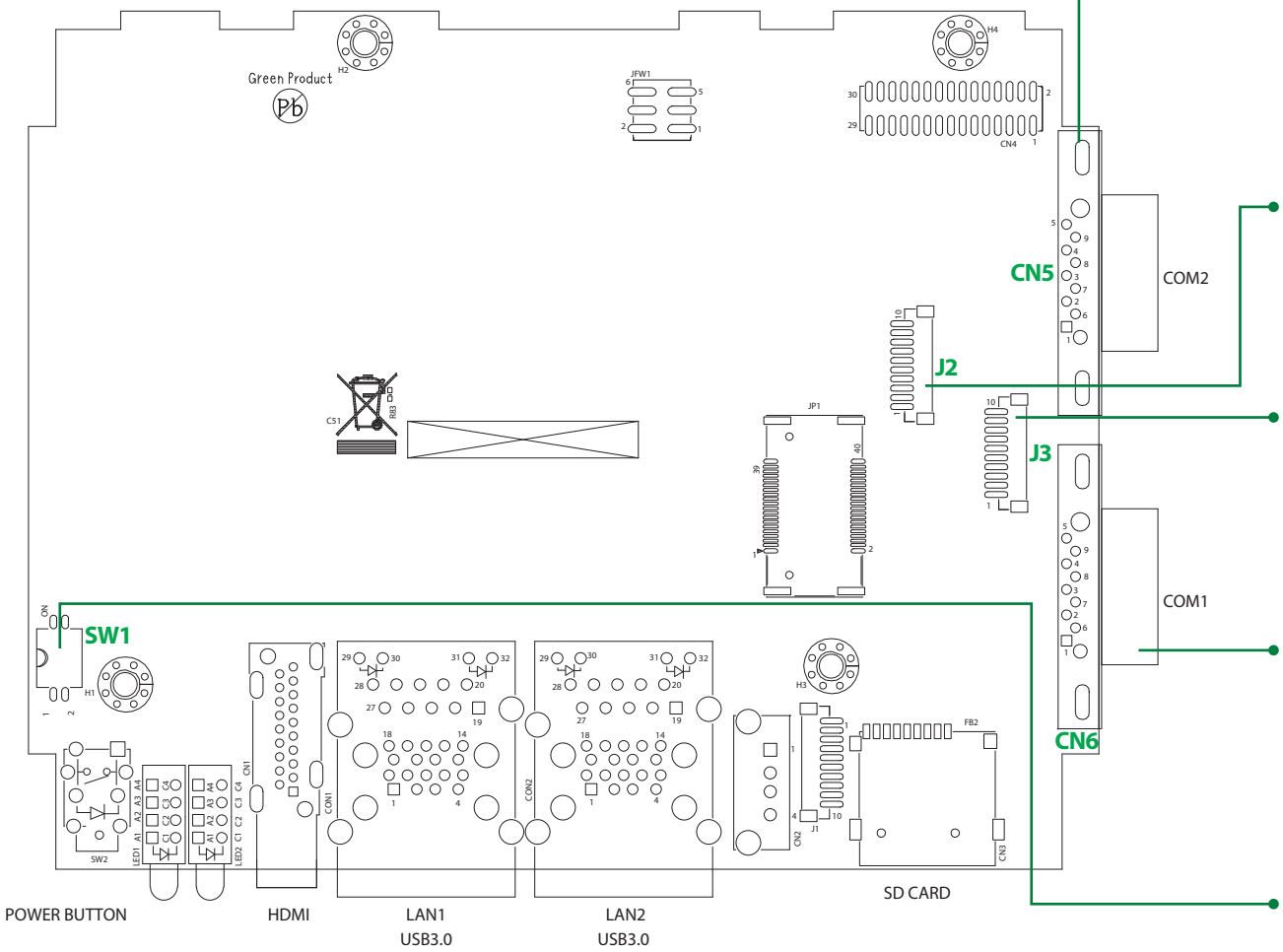
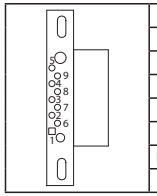
NIFE 105 Quick Reference Guide

Ver A. (P/N: 60177A0580X00)

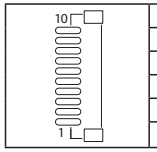


CN5: COM2 Connector



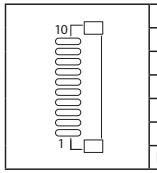
Pin	Definition	Pin	Definition
1	COM2_DCD	2	COM 2_RXD
3	COM 2_TXD	4	COM 2_DTR
5	GND	6	COM 2_DSR
7	COM 2_RTS	8	COM 2_CTS
9	COM 2_RI		
Mh1	CH_GND_REAL	Mh2	CH_GND_REAL
Nh1	NA	Nh2	NA

J2: GPIO Internal Pin Header



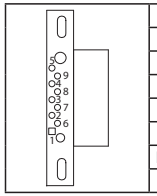
Pin	Definition	Pin	Definition
1	VCC5	2	GND
3	ICH_GPO0_OUT	4	ICH_GPI0_IN
5	ICH_GPO1_OUT	6	ICH_GPI1_IN
7	ICH_GPO2_OUT	8	ICH_GPI2_IN
9	ICH_GPO3_OUT	10	ICH_GPI3_IN

J3: COM3/4 Internal Pin Header (TX/RX//RTS/CTS Only)



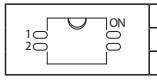
Pin	Definition	Pin	Definition
1	COM3_RXD	2	COM 3_TXD
3	COM 3_RTS#	4	COM 3_CTS#
5	COM 4_RXD	6	COM 4_TXD
7	COM 4_RTS#	8	COM 4_CTS#
9	GND	10	GND
Mh1	GND	Mh2	GND

CN6: COM1 Connector



Pin	Definition	Pin	Definition
1	COM1_DCD	2	COM1_RXD
3	COM1_TXD	4	COM1_DTR
5	GND	6	COM1_DSR
7	COM1_RTS	8	COM1_CTS
9	COM1_RI		
Mh1	CH_GND_REAL	Mh2	CH_GND_REAL
Nh1	NA	Nh2	NA

SW1: AT/ATX Switch



Pin	AT	ATX (*)
1	ON	OFF
2	OFF	ON

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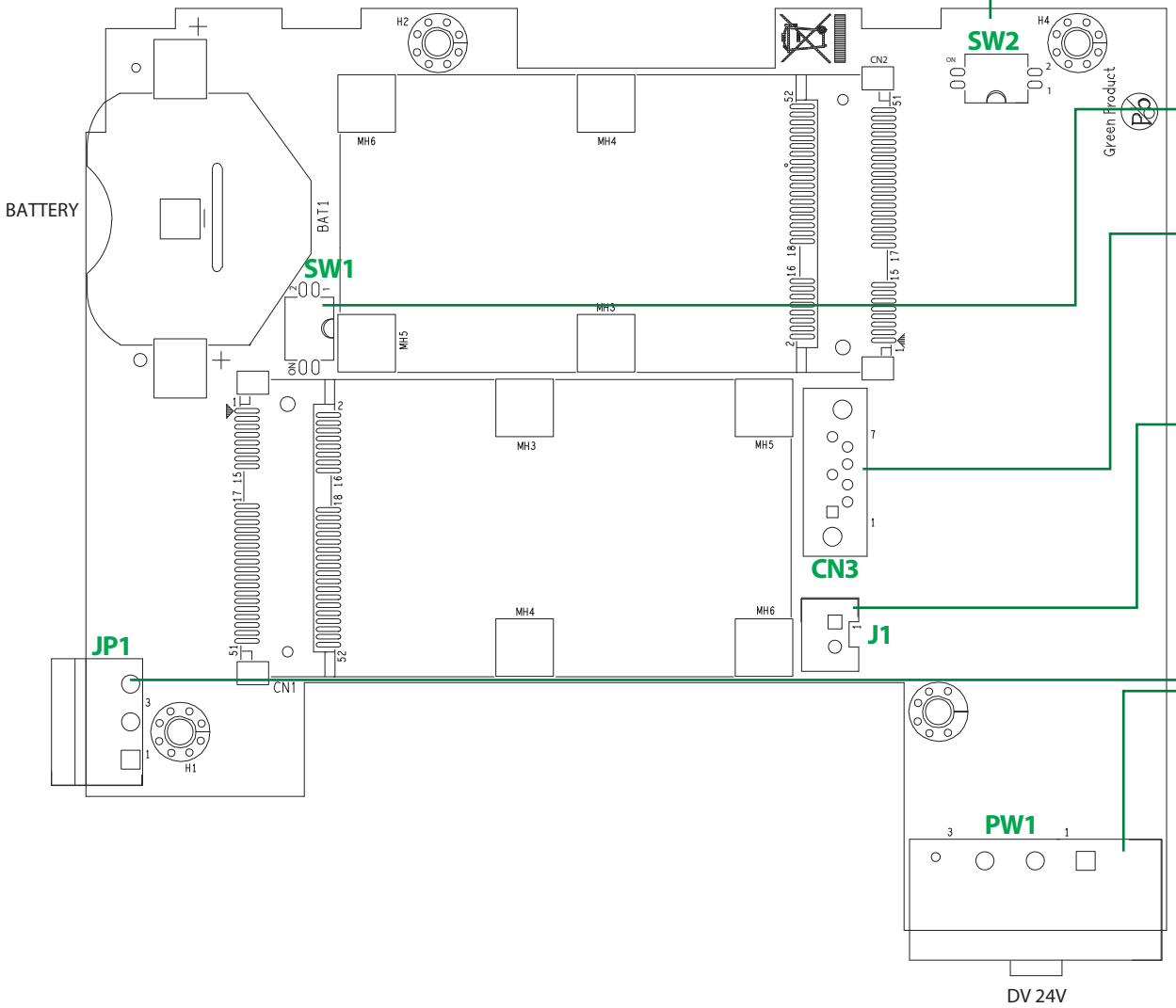

SW2: Clear CMOS Switch

Pin	Definition	Pin	Definition
1	RTEST#	2	SRTCRST#
3	GND	4	GND

Pin	Function Description
1-2(*)	Normal
3-4	Clear BIOS Setting

SW1: Clear CMOS Switch

Pin	Definition	Pin	Definition
1	UIM_PWR1	2	UIM_PWR2
3	UIM_PWR	4	UIM_PWR

	MiniPCIe CN1(*)	MiniPCIe CN2
1	ON	OFF
2	OFF	ON

CN3: SATA Signal Connector

Pin	Definition	Pin	Definition
1	GND	2	SATA_TXP1
3	SATA_TXN1	4	GND
5	SATA_RXN1	6	SATA_RXP1
7	GND		
MH1	GND	MH2	GND

J1: SATA Power Connector

Pin	Definition
1	VCC5
2	GND

JP1: Remote Switch/S3 Internal Pin Header

Pin	Definition
1	S3
2	GND
3	Remote Switch

PW1: Power Input Connector (DC Input)

Pin	Definition
1	+DC 24V
2	-
3	GND

Quick reference guide is subject to change without prior notice. For any update, please visit our website: www.nexcom.com

□=Pin1 [*]=Default setting