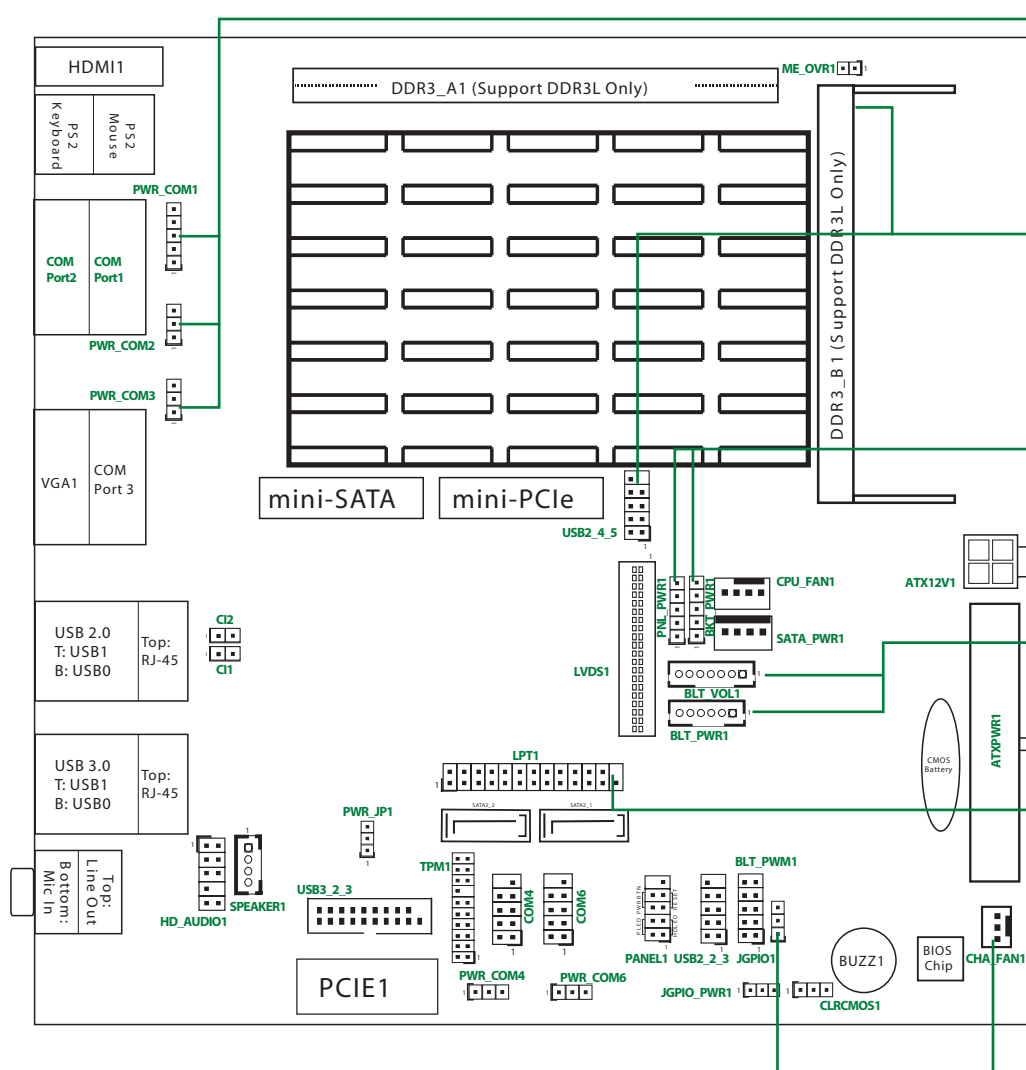


NEX 617 Quick Reference Guide

Ver A. (P/N: 60177A0421X00)



PWR_COM1 (For COM Port1): COM Port PWR Setting Jumpers

Pin	Definition
1-2	5V
2-3	+12V
3-4	+12V
4-5	+5VSB

USB2_4_5: USB2.0 Connector

Pin	Definition	Pin	Definition
1	USB_PWR	2	-A
3	+A	4	GND
5		6	USB_PWR
7	-B	8	+B
9	GND	10	DUMMY

LCD_VCC/PNL_PWR1: Panel Power Selection

Pin	Definition
1-2	LVDD: +3V
2-3	LVDD: +5V
4-5	LVDD: +12V

BLT_PWR1: Backlight Power Connector

Pin	Definition	Pin	Definition
1	GND	2	GND
3	BL CTL	4	BL EN
5	LCD_BLT_VCC	6	LCD_BLT_VCC

LPT1: Printer Port Header

Pin	Definition	Pin	Definition	Pin	Definition	Pin	Definition
1	STB#	2	SPD0	3	SPD1	4	SPD2
5	SPD3	6	SPD4	7	SPD5	8	SPD6
9	SPD7	10	ACK#	11	BUSY	12	PE
13	SLCT	14	AFD#	15	ERROR#	16	PINT#
17	SLIN#	18	GND	19	GND	20	GND
21	GND	22	GND	23	GND	24	GND
25	GND	26	GND				

BLT_PWM1

Pin	Definition
1-2	+3V Level
2-3	+5V Level

PWR_COM2 (For COM Port2) PWR_COM3 (For COM Port3)

Pin	Definition
1-2	5V
2-3	+12V

ME Override (Security Flash Descriptors)

Pin	Definition
Short	Override
Open	Normal Operation

BKT_PWR1/LCD_BLT_VCC: Backlight Power Selection

Pin	Definition
1-2	LCD_BLT_VCC: +5V
2-3	LCD_BLT_VCC: +12V
4-5	LCD_BLT_VCC: DC_IN

SATA_PWR1: SATA Power Output Connector

Pin	Definition	Pin	Definition
1	+5V	2	GND
3	GND	4	+12V

CHA_FAN1: 3-Pin Chassis FAN Connector

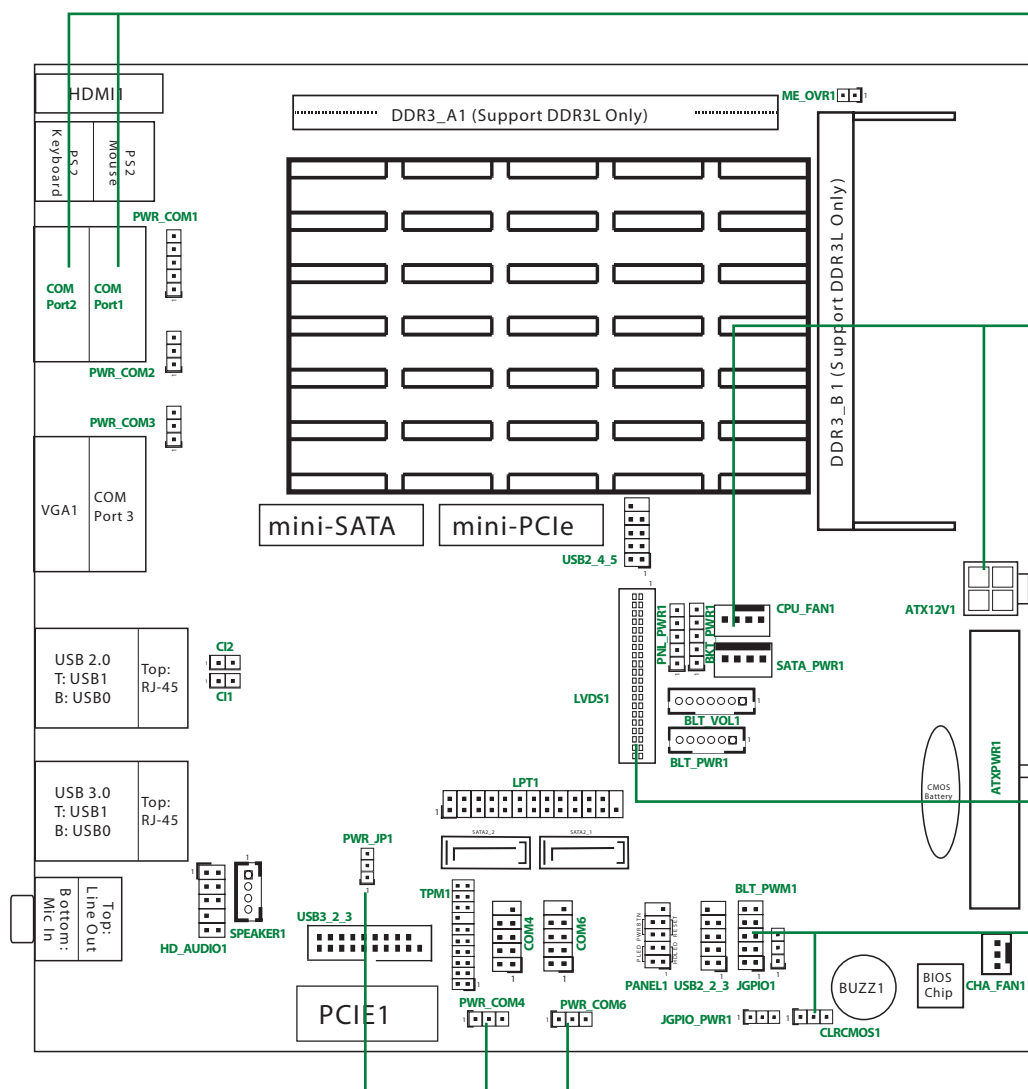
Pin	Definition
1	FAN_SPEED
2	+12V
3	GND

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□ = Pin1
[*] = Default setting

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COM Port 1/COM Port 2: (RS232/422/485)

Pin	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	COM1: +5V/+12V/+5VSB COM2, 3: +5V/+12V	COM1: +5V/+12V/+5VSB COM2, 3: +5V/+12V	COM1: +5V/+12V/+5VSB COM2, 3: +5V/+12V

CPU_FAN1: 4-Pin CPU FAN Connector

Pin	Definition
1	FAN_SPEED_CONTROL
2	CPU_FAN_SPEED
3	+12V
4	GND

ATX12V1: ATX Power Connector

Pin	Definition
1-2	GND
3-4	DC Input

LVDS1: LVDS Panel Connector

Pin	Definition	Pin	Definition	Pin	Definition	Pin	Definition
1	LCD_VCC	2	LCD_VCC	3	+3.3V	4	LDDC_CLK
5	LDDC_DATA	6	LVDS_A_DATA0#	7	LVDS_A_DATA0	8	GND
9	LVDS_A_DATA1#	10	LVDS_A_DATA1	11	GND	12	LVDS_A_DATA2#
13	LVDS_A_DATA2	14	GND	15	LVDS_A_DATA3#	16	LVDS_A_DATA3
17	GND	18	LVDS_A_CLK#	19	LVDS_A_CLK	20	GND
21	LVDS_B_DATA0#	22	LVDS_B_DATA0	23	GND	24	LVDS_B_DATA1#
25	LVDS_B_DATA1	26	GND	27	LVDS_B_DATA2#	28	LVDS_B_DATA2
29	DPLVDD_EN	30	LVDS_B_DATA3#	31	LVDS_B_DATA3	32	GND
33	LVDS_B_CLK#	34	LVDS_B_CLK	35	GND	36	CON_LBKLT_EN
37	CON_LBKLT_CTL	38	LCD_BLT_VCC	39	LCD_BLT_VCC	40	LCD_BLT_VCC

JGPIO_PWR1: Digital Input/Output Power Select

Pin	Definition
1-2	+12V
2-3	+5V

CLRCMOS1: Clear CMOS Header

Pin	Definition
1-2	Normal
2-3	Clear CMOS

PWR_JP1: ATX/AT Mode Selection

Pin	Definition
1-2	AT Mode
2-3	ATX Mode

PWR_COM4/PWR_COM6: COM Port PWR Setting Jumpers

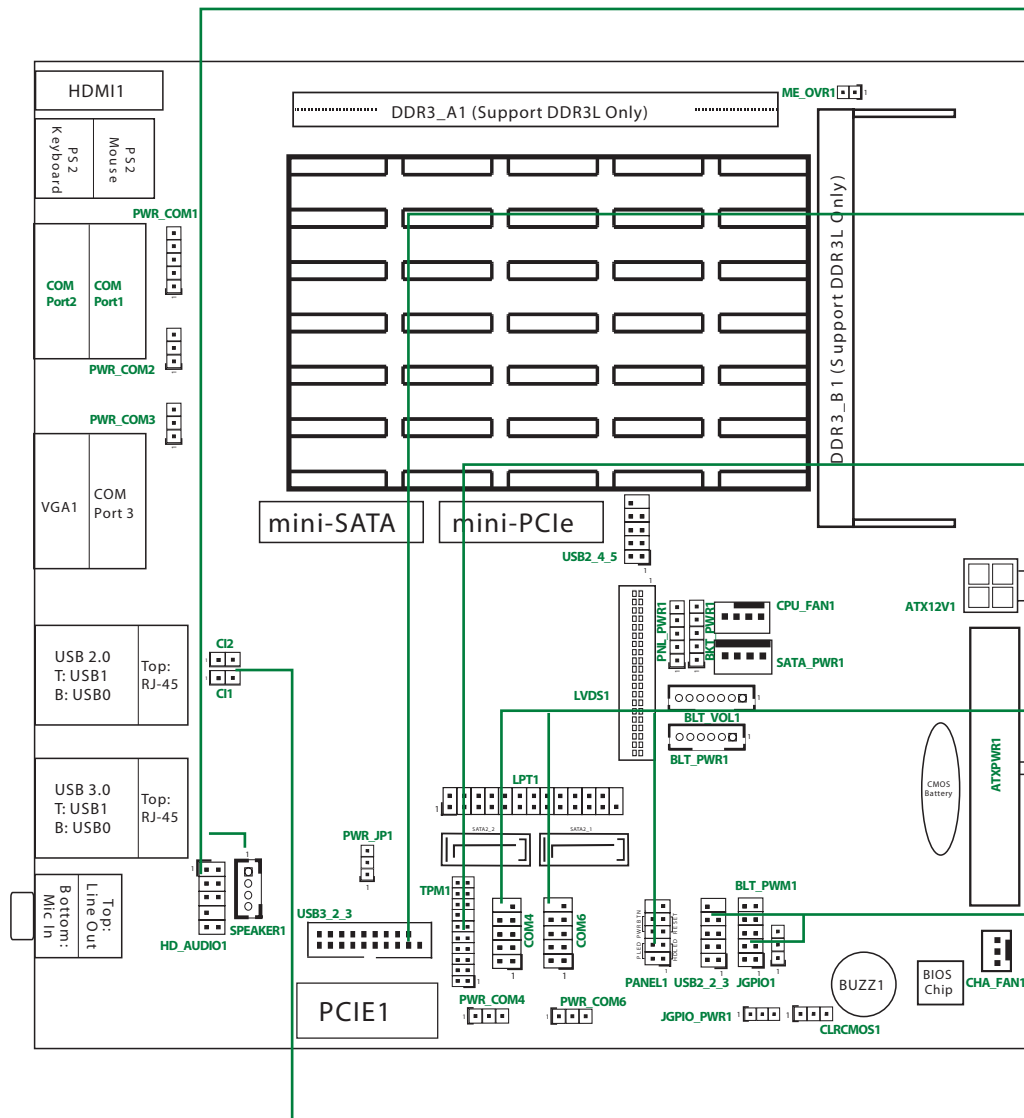
Pin	Definition
1-2	+5V
2-3	+12V

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□=Pin1
[*]=Default setting

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HD_AUDIO1: Front Panel Audio Header

Pin	Definition	Pin	Definition
1	MIC2_L	2	MIC2_R
3	OUT2_R	4	J_SENSE
5	OUT2_L	6	GND
7	PRESENCE#	8	MIC_RET
9		10	OUT_RET

SPEAKER1: 3W Audio AMP Output Wafer

Pin	Definition
1	OUTLN
2	OUTLP
3	OUTRP
4	OUTRN

USB3_2_3: USB 3.0 Connector

Pin	Definition	Pin	Definition	Pin	Definition
1	Dummy	2	PORT B USB	3	PORT B USB
4	GND	5	PORT B USB	6	PORT B USB
7	GND	8	PORT B USB	9	PORT B USB
10	Vbus	11	PORT A USB	12	PORT A USB
13	GND	14	PORT A USB	15	PORT A USB
16	GND	17	PORT A USB	18	PORT A USB
19	Vbus	20			

TPM Header

Pin	Definition	Pin	Definition	Pin	Definition
1	PCICLK	2	FRAME	3	PCIRST#
4	LAD3	5	+3V	6	LAD0
7		8	+3VSB	9	GND
10	+5V	11	GND	12	SMB_CLK_MAIN
13	SMB_DATA_MAIN	14	LAD2	15	LAD1
16	GND	17	S_PWRDWN#	18	SERIRQ#
19	F_CLKRUN#	20	48MHz		

COM4/6: COM4, 6 Headers (RS232)

Pin	Definition	Pin	Definition
1	DDCD#	2	TTXD
3	GND	4	RRTS#
5	CM_P9	6	RRXD
7	DDTR#	8	DDSR#
9	CCTS#		

PANEL1: System Panel Header

Pin	Definition	Pin	Definition
1	HDLED+	2	HDLED-
3	GND	4	RESET#
5	GND	6	PLED+
7	PLED-	8	PWRBTN#
9	GND		

USB2_2_3: USB2.0 Connector

Pin	Definition	Pin	Definition
1	USB_PWR	2	-A
3	+A	4	GND
5		6	USB_PWR
7	-B	8	+B
9	GND	10	DUMMY

JGPIOW1: Digital Input/Output Pin Header

Pin	Definition	Pin	Definition
1	SIO_GP24	2	SIO_GP20
3	SIO_GP25	4	SIO_GP21
5	SIO_GP26	6	SIO_GP22
7	SIO_GP27	8	SIO_GP23
9	JGPIOW_PWR	10	GND

C11/C12: Chassis Intrusion Headers

Pin	Definition
1	Signal
2	GND

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□=Pin1
[*]=Default setting