

NBP1412-64

Connector	Function
KJP1, KJP2, CN10, CN11	AT K/B CONNECTOR
CN6, CN8	FAN CONNECTOR
CN2	WAKE UP CONNECTOR
CN4	NEXCARE VOLTAGE DETECT CONNECTOR
JP1	ATX POWER ON/OFF CONNECTOR
JP2	ATX POWER ON/OFF SW SELECTOR
CN1, CN9	ATX POWER CONNECTOR
CN5	POWER TERMINAL BLOCK
CN7	FAN SPEED DETECT CONNECTOR
S1 (SVIO1)	SVIO1 FOR J12, J11, J10 INTERFACE VOLTAGE SELECT
S2 (PVIO)	PVIO FOR CPU SLOT INTERFACE VOLTAGE SELECT
S3 (SVIO2)	SVIO2 FOR J9, J8, J7 INTERFACE VOLTAGE SELECT
S4	ATX POWER ON/OFF SW
S5 (SVIO3)	SVIO3 FOR J6, J5, J4, J3, J2, J1 INTERFACE VOLTAGE SELECT

Pin Definition

KJP1, KJP2, CN10, CN11 – Keyboard Connector

Pin No.	Description	Pin No.	Description
1	KBCLK	4	GND
2	KBDATA	5	+5V
3	NC		

CN6, CN8 – FAN Connector

Pin No.	Description	Pin No.	Description
1	GND	3	NC
2	+12V		

CN2 – Wake Up Connector

Pin No.	Description	Pin No.	Description
1	+5VSB	3	WAKEUP
2	GND		

CN4 – Nexcare Voltage Detect Connector

Pin No.	Description	Pin No.	Description
1	5VSB	4	-12V
2	+3.3V	5	GND
3	-5V		

JP1 – Power ON/OFF Connector

Pin No.	Description	Pin No.	Description
1 & 2	w/o lock SW*	2 & 3	w/ lock SW

JP2 – Power ON/OFF Connector

Pin No.	Description	Pin No.	Description
1 & 2	w/o lock SW*	2 & 3	w/ lock SW

CN1, CN9 – ATX Power Connector

Pin No.	Description	Pin No.	Description
1	+3.3V	11	+3.3V
2	+3.3V	12	-12V
3	GND	13	GND
4	+5V	14	POWER ON
5	GND	15	GND
6	+5V	16	GND
7	GND	17	GND
8	POWER OK	18	-5V
9	+5VSB	19	+5V
10	+12V	20	+5V

CN5 – Terminal Block Connector

Pin No.	Description	Pin No.	Description
1	+5V	4	+12V
2	-5V	5	+3.3V
3	-12V	6	GND

CN7 – FAN Speed Detect Connector

Pin No.	Description	Pin No.	Description
1	FAN1 (CN6) SPEED OUT	3	GND
2	FAN2 (CN8) SPEED OUT		

S1 (SVIO1) – SVIO1 FOR J12, J11, J10 INTERFACE VOLTAGE SELECT

Pin No.	Description	Pin No.	Description
1	+5V	3	+3.3V*
2	COMMON		

S2 (PVIO) – PVIO FOR CPU SLOT INTERFACE VOLTAGE SELECT

Pin No.	Description	Pin No.	Description
1	+5V	3	+3.3V*
2	COMMON		

S3 (SVIO2) – SVIO2 FOR J9, J8, J7 INTERFACE VOLTAGE SELECT

Pin No.	Description	Pin No.	Description
1	+5V	3	+3.3V*
2	COMMON		

S5 (SVIO3) – SVIO3 FOR J6, J5, J4, J3, J2, J1 INTERFACE VOLTAGE SELECT

Pin No.	Description	Pin No.	Description
1	+5V	3	+3.3V*
2	COMMON		

* Means default setting

- NOTE: 1. Bridge 1 drives PCI slot J10~12, 66MHz/33MHz (64bit/32bit)
 2. Bridge 2 drives PCI slot J7~9, 66MHz/33MHz (64bit/32bit)
 3. Bridge 2 drives PCI slot J1~6, 33MHz (64bit / 32bit)