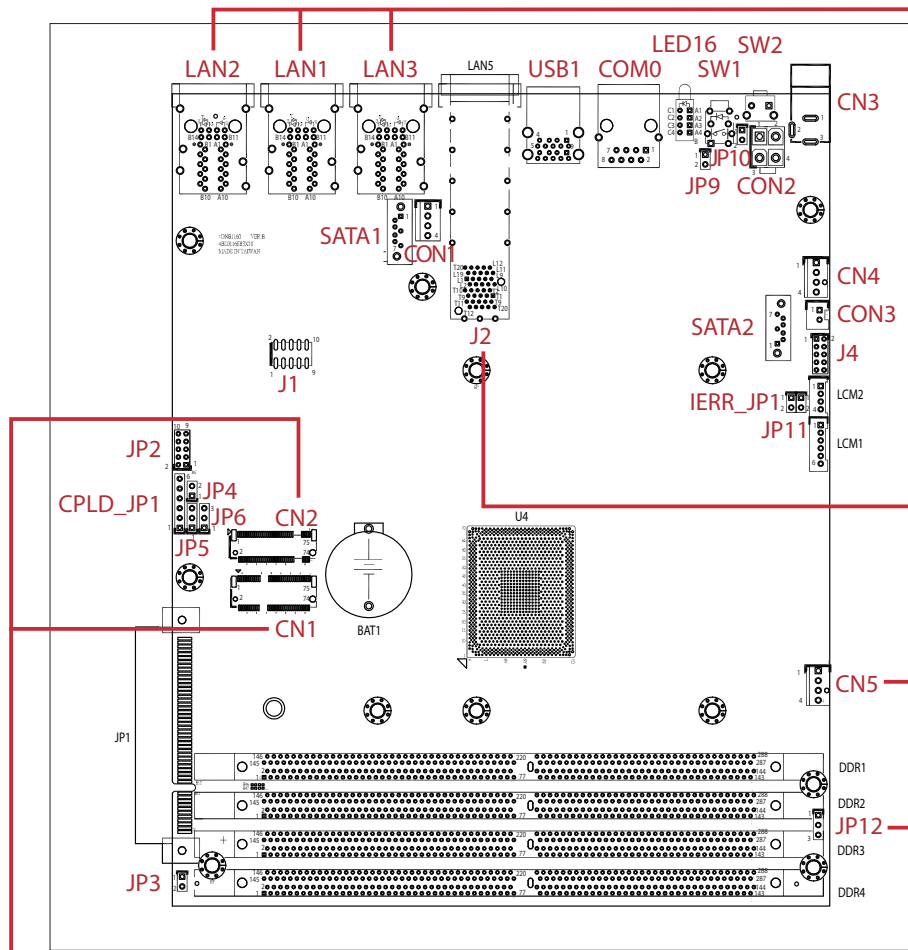


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LAN2, LAN1, LAN3: LAN

Pin	Signal	Pin	Signal
A1	Port4_TX0P	B1	Port3_TX0P
A2	Port4_TX0N	B2	Port3_TX0N
A3	Port4_TX1P	B3	Port3_TX1P
A4	Port4_TX1N	B4	Port3_TX1N
A5	Port4_TX2P	B5	Port3_TX2P
A6	Port4_TX2N	B6	Port3_TX2N
A7	Port4_TX3P	B7	Port3_TX3P
A8	Port4_TX3N	B8	Port3_TX3N
A9	GND	B9	GND
A10	GND	B10	GND
A11	P3V3	B11	P3V3
A12	88E1543_PORT4_ACT_N	B12	88E1543_PORT3_ACT_N
A13	88E1543_PORT4_L100_N	B13	88E1543_PORT3_L100_N
A14	88E1543_PORT4_L1000_N	B14	88E1543_PORT3_L1000_N
MH1	CGND	MH4	CGND
MH2	CGND	MH5	CGND
MH3	CGND	MH6	CGND

J2 (2 ports SFP+): SFP+

Pin	Signal	Pin	Signal
T1	GND	L1	GND
T2	TxFault1	L2	TxFault2
T3	TxDisable1	L3	TxDisable2
T4	P10G_Sda1	L4	P10G_Sda2
T5	P10G_Scl1	L5	P10G_Scl2
T6	ModABS1_L	L6	ModABS2_L
T7	1_RS0	L7	2_RS0
T8	RxLOS1	L8	RxLOS2
T9	1_RS1	L9	2_RS1
T10	GND	L10	GND
T11	GND	L11	GND
T12	1_SFP+_RD-	L12	2_SFP+_RD-
T13	1_SFP+_RD+	L13	2_SFP+_RD+
T14	GND	L14	GND
T15	3P3V_SFP1_R	L15	3P3V_SFP2_R
T16	3P3V_SFP1_T	L16	3P3V_SFP2_T
T17	GND	L17	GND
T18	1_SFP+_TD+	L18	2_SFP+_TD+
T19	1_SFP+_TD-	L19	2_SFP+_TD-
T20	GND	L20	GND

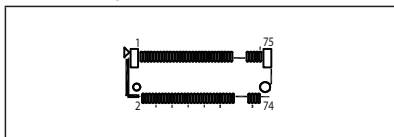
CN5 (1 X 4 pin header): CPU Fan

Pin	Description	Pin	Description
1	GND	3	FAN1_IN_CON
2	P12V	4	FAN1_PWM_OUT

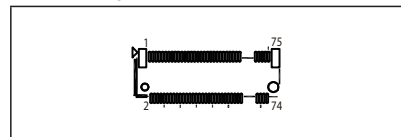
JP12 (1 X 3 pin header): PWM

Pin	Description	Pin	Description
1	SDA	3	GND
2	SDA		

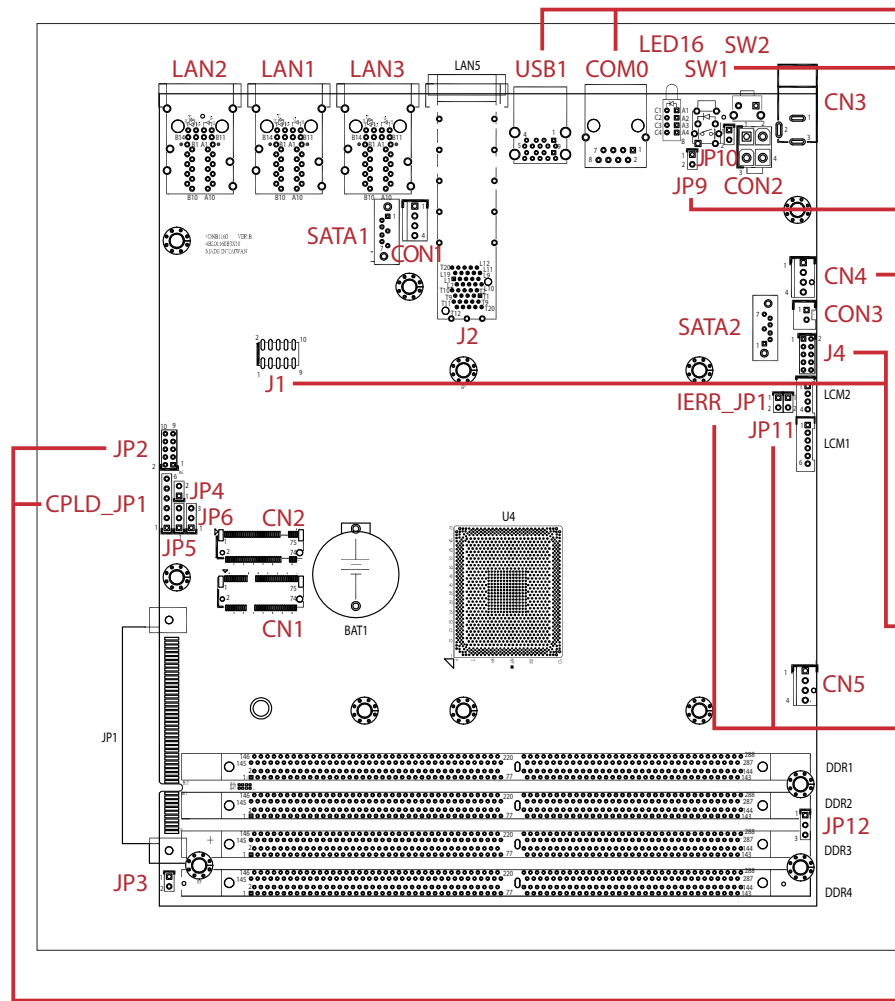
CN2 (M key/SSD): M.2



CN1 (E key/WIFI, BT): M.2



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USB1: USB 3.0

Pin	Signal	Pin	Signal
1	P5V_USB	10	P5V_USB
2	USB2_P0-	11	USB2_P1-
3	USB2_P0+	12	USB2_P1+
4	GND	13	GND
5	USB3_RX_D0-	14	USB3_RX_D1-
6	USB3_RX_D0+	15	USB3_RX_D1+
7	GND	16	GND
8	USB3_TX_D0-	17	USB3_TX_D1-
9	USB3_TX_D0+	18	USB3_TX_D1+

COM0: System Power Button

Pin	Signal
1	SP_RTS1
2	SP_DTR1
3	SP_TXD1
4	GND
5	SP_DCD1
6	SP_RXD1
7	SP_DSR1
8	SP_DSR1

JP9: System Power Button

Pin	Description
1	PWR_BTN_CAL_N
2	GND

SW1: System Power Button

Pin	Description	Pin	Description
1	PWR_BTN_CAL_N	3	GND
2	GND	4	PWR_BTN_CAL_N

CN4 (1 X 4 pin header): System Fan

Pin	Description	Pin	Description
1	GND	3	FAN2_IN_CON
2	P12V	4	FAN2_PWM_OUT

J1 (2 X 5 pin header): Boundary Scan

Pin	Description	Pin	Description
1	GND	6	I350A_JTAG_TDI
2	I350A_JTAG_TRST	7	GND
3	GND	8	I350A_JTAG_TMS
4	I350A_JTAG_TDO	9	GND
5	JTAG_PWR_SOC	10	I350A_JTAG_TCK

J4 (2 X 5 pin header): Boundary Scan

Pin	Description	Pin	Description
1	GND	6	SOC_JTAG_TDI
2	SOC_JTAG_TRST	7	GND
3	GND	8	SOC_JTAG_TMS
4	SOC_JTAG_TDO	9	GND
5	JTAG_PWR_SOC	10	SOC_JTAG_TCK

IERR_JP1: Flash Security Override

Pin	Description
1	NC
2	Security locked (default)
1-2	Security unlocked

JP11 (1 X 2 pin header): Me Recover Mode

Pin	Description
1	NC
2	NORMAL (default)
1-2	Me Recover Mode

JP2 (2 X 5 pin header): GPIO

Pin	Description	Pin	Description
1	P5V	6	SIO_GPOUT2
2	GND	7	SIO_GPIN3
3	SIO_GPIN1	8	SIO_GPOUT3
4	SIO_GPOUT1	9	SIO_GPIN4
5	SIO_GPIN2	10	SIO_GPOUT4

CPLD_JP1 (1 X 6 pin header): CPLD JTAG

Pin	Description	Pin	Description
1	P3V_STBY	6	JTAG_PLD_TDO
2	GND	7	JTAG_PLD_TDI
3	JTAG_PLD_TCK	8	JTAG_PLD_TMS

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